Features

- The output voltage can be adjusted to 36V
- Low dynamic output impedance ,its typical value is 0.2Ω
- Trapping current capability is 1 to 100mA
- Low output noise voltage

1.Reference
2.Anode
3.Cathode



SOT-89

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TL431	SOT-89	431	1000

Maxmim Ratings (Ta=25℃unless otherwise noted)

Parameter	Symbol	Value	Unit
Cathode Voltage	V_{KA}	36	V
Cathode Current Range (Continuous)	I_{KA}	-100-+150	mA
Reference Input Current Range	Iref	0.05-+10	mA
Power Dissipation	P_D	500	mW
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	250	℃/W
Operating Ambient Temperature Range	T_{opr}	-25~+85	℃
Storage temperature Range	Tstg	-65-+150	℃
Operating JunctionTemperature	T_j	150	℃

Electrcal Charcteristics (Ta=25℃unless otherwise specified)

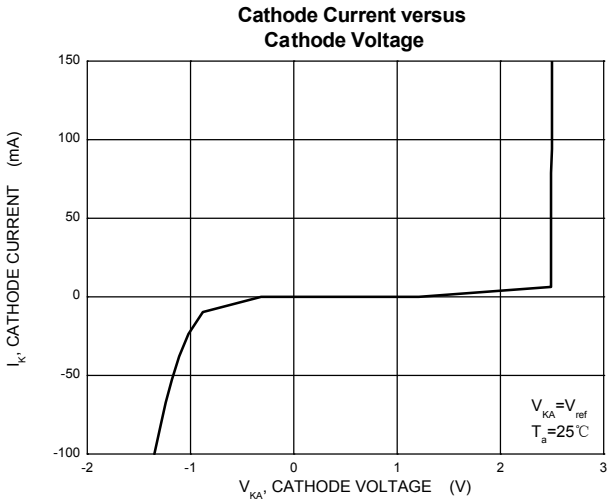
Parameter	Symbol	Test conditions		Min	Typ	Max	Unit
Reference input voltage	V_{ref}	$V_{KA}=V_{REF}$, I_{KA}=10mA		2.475	2.5	2.525	V
Deviation of reference Input voltage over temperature (note)	$\triangle V_{ref}/\triangle T$	$V_{KA}=V_{REF}$, I_{KA}=10mA $T_{MIN}\leq T_a\leq T_{MAX}$			4.5	17	mV
Ratio of change in reference Input voltage to the change in cathode voltage	$\triangle V_{ref}/\triangle V_{KA}$	I_{KA}=10mA	$\triangle V_{KA}$ =10V~V_{REF}		-1.0	-2.7	mV/V
			$\triangle V_{KA}$ =36V~10V		-0.5	-2.0	mV/V
Reference input current	I_{ref}	I_{KA}= 10mA,R_1=10kΩ R_2=∞			1.5	4	μA
Deviation of reference input current over full temperature range	$\triangle I_{ref}/\triangle T$	I_{KA}=10mA, R_1=10kΩ R_2=∞ T_A=-25 to 85℃			0.4	1.2	μA
Minimum cathode current for regulation	$I_{KA(min)}$	$V_{KA}=V_{REF}$			0.45	1.0	mA
Off-state cathode current	$I_{KA(OFF)}$	V_{KA}=36V ,V_{REF}=0			0.05	1.0	μA
Dynamic impedance	Z_{KA}	$V_{KA}=V_{REF}$, I_{KA}=1 to 100mA f≤1.0kHz			0.15	0.5	Ω

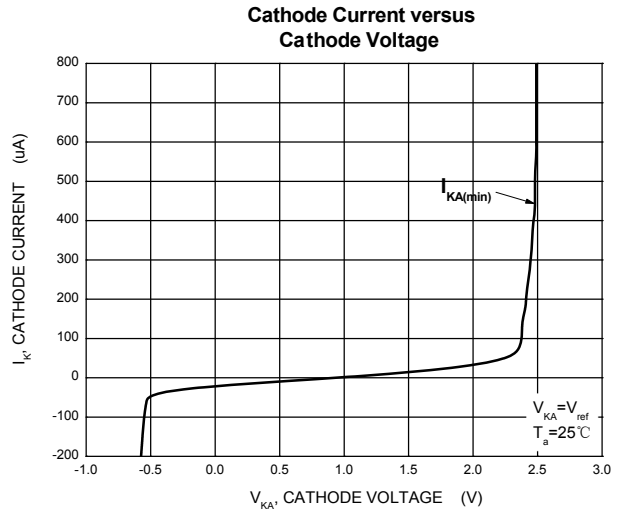
Note:T_{MIN}=-25℃ ,T_{MAX}=+85℃

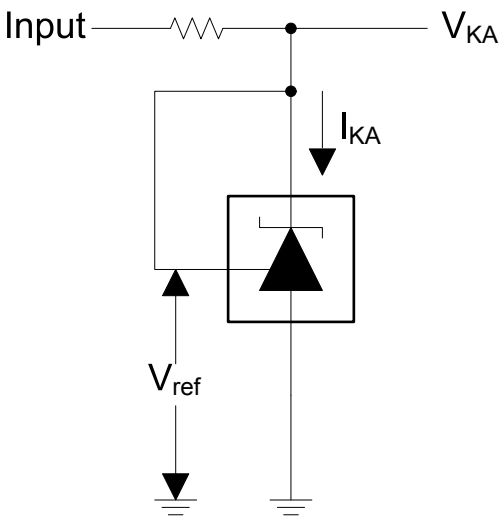
Classification Of Vref

Rank	0.5%	1%
Range	2.487-2.513	2.475-2.525

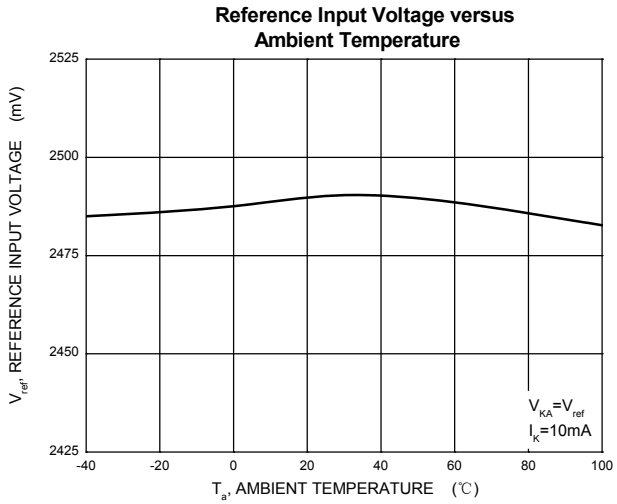
Typical Characteristics

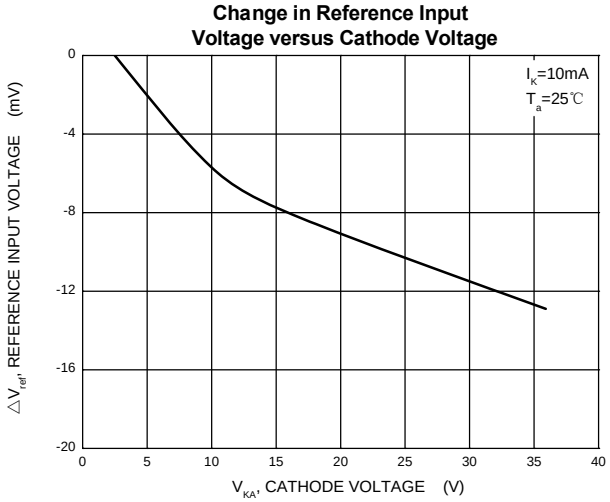


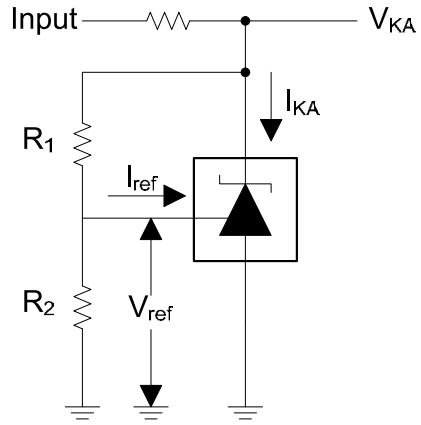




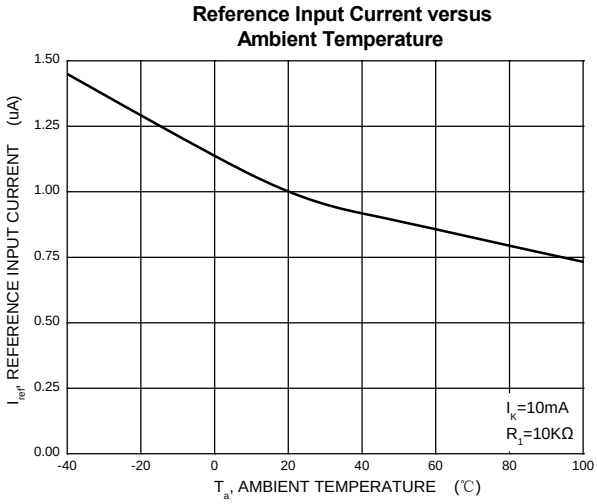
Test Circuit for $V_{KA}=V_{ref}$

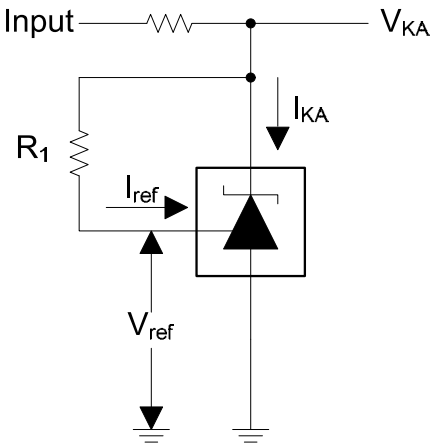




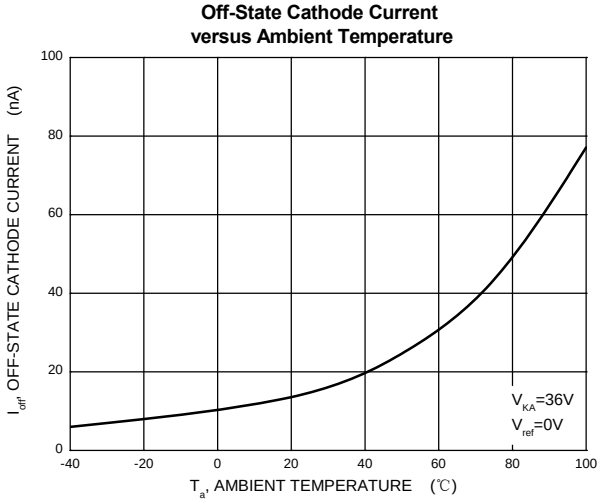


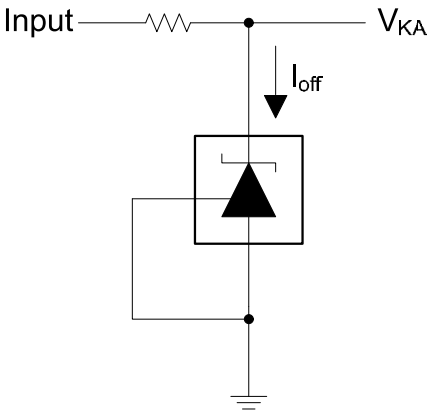
Test Circuit for $V_{KA}=V_{ref}(1+R1/R2)+R1*I_{ref}$





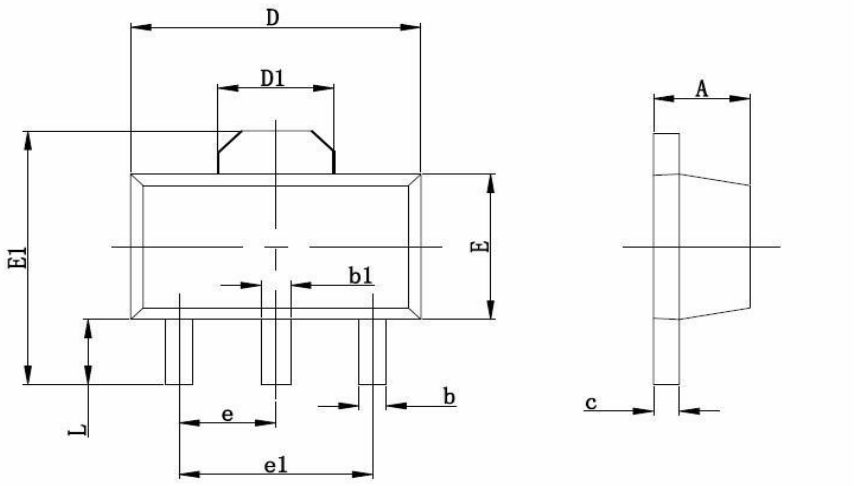
Test Circuit for I_{ref}





Test Circuit for I_{off}

SOT-89 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF.		0.061 REF.	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP.		0.060 TYP.	
e1	3.000 TYP.		0.118 TYP.	
L	0.900	1.200	0.035	0.047

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