

5V 5-Channel Micro STM&DC Motor Driver

FEATURES

- Built-in 5-channel Motor Driver
- 1~4CH Control Two Stepper Motors or Four DC Motor, and the 5CH Channel Control One DC Motor or Coil
- 1CH/2CH & 3CH/4CH are Capable of Micro-steps Mode to Drive Maximum 512 Resolution Micro-steps
- 2.4V to 5.5V Operating Voltage
- Maximum Continuous Current: 0.7A
- VM Under-voltage Lockout (UVLO)
- Thermal Shutdown (TSD)
- Low-power Sleep Mode
- Automatic Fault Recovery
- A Small 20-pin QFN Package Type Has Been Adopted

APPLICATIONS

- DC motor & STM motor
- PTZ of IPC
- Robot

TYPICAL APPILCATION

GENERAL DESCRIPTION

The TMI8150B is an integrated DC motor driver, applied to low input voltage working system. The TMI8150B uses serial communication to control 5 channels of motors, which supports PTZ motor and IR cut filter change motor. 1~4CH can drive 2 stepper motor of PTZ (X and Y axis), X-axis and Y-axis are controlled by orthogonal SPWM and support forward and reverse rotation. 5CH control IR-cut.

The current consumption can be hold at quite low level during standby or operation. In low-power sleep mode, the current is only 16 μ A.

The package form of TMI8150B is QFN3x3-20L, which conforms to ROHS specifications, and the frame is 100% lead-free.

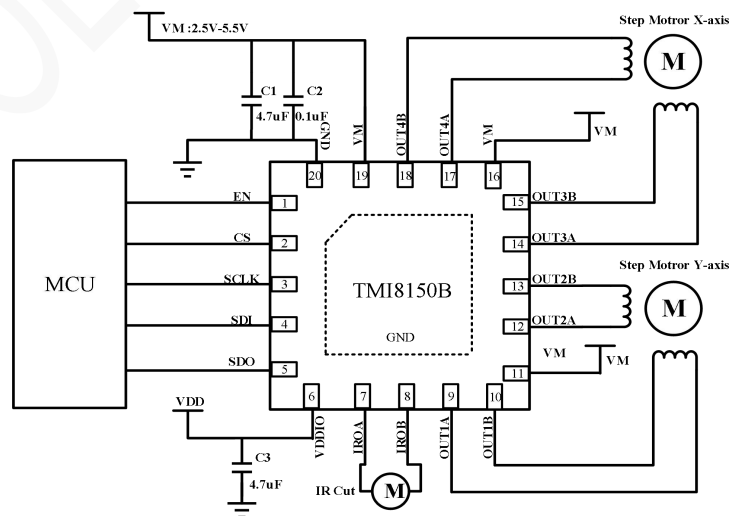
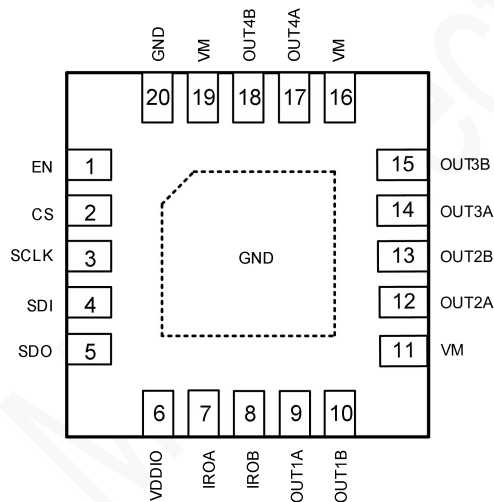


Figure 1. Application Circuit

ABSOLUTE MAXIMUM RATINGS (Note 1)

Parameter	Value	Unit
Power Supply Voltage (VM)	-0.3~6	V
Logic power supply(VDDIO)	-0.3~6	V
EN, SCLK, SDI, CS Input Voltage	-0.3~VM	V
Maximum Operating Junction Temperature T_J (Note 2)	150	°C
Storage Temperature T_{sdg}	-55~150	°C
Operating Temperature T_{op}	-40~85	°C
Maximum Chip Thermal Resistance θ_{JA}	80	°C/W
Maximum Pin Solder Temperature (soldering 10s)	260	°C

PACKAGE/ORDER INFORMATION



QFN3X3-20L

TMI8150B

Top Mark: TMI8150B/XXXXX (TMI8150B: Device Code, XXXXX: Inside Code)

Part Number	Package	Top mark	Quantity/ Reel
TMI8150B	QFN3x3-20L	TMI 8150B XXXXX	5000

TMI8150B products meet lead-free requirements and ROHS standards

PIN DISCRIPTIONS

Pin	Name	Function
1	EN	Enable pin. Pull-down 100kΩ resister. When the input logic voltage is low, the chip function disabled. When the input logic voltage is high, the chip function enables.
2	CS	Chip select signal input pin. Pull-up 100kΩ resister. Default state is high.
3	SCLK	SPI clock input pin. Pull-down 100kΩ resister. Default state is low.
4	SDI	SPI data input pin. Pull-down 100kΩ resister. Default state is low.
5	SDO	SPI data output pin.
6	VDDIO	1.8V to 5.5V Logic power supply. Connect a 4.7μF Chip capacitors to ground.
7	IROA	IR Cut drive output pin A.
8	IROB	IR Cut drive output pin B.
9	OUT1A	PZT drive output pin 1A.
10	OUT1B	PZT drive output pin 1B.
11	VM	2.4V to 5.5V power supply.
12	OUT2A	PZT drive output pin 2A.
13	OUT2B	PZT drive output pin 2B.
14	OUT3A	PZT drive output pin 3A.
15	OUT3B	PZT drive output pin 3B.
16	VM	2.4V to 5.5V power supply.
17	OUT4A	PZT drive output pin 4A.
18	OUT4B	PZT drive output pin 4B.
19	VM	2.4V to 5.5V power supply.
20	GND	Logic ground. Connect to board ground.
EPAD	GND	Logic ground. Connect to board ground.

ESD RATING

Items	Description	Value		Unit
V _{ESD}	Human body model	OUTx	±7000	V
		Other Pins	±4000	
	Charged device model (CDM)	±750		V

JEDEC specification JS-001

RECOMMENDED OPERATING CONDITIONS

Items	Description	Min	Max	Unit
VM	Power supply voltage range	2.5	5.5	V
VDDIO	Logic power supply range	1.8	5.5	V
V _{input}	Logic signal voltage range	0	VM	V

ELECTRICAL CHARACTERISTICS

(If no special regulations VM=5V, Ta=25°C)

Parameter	Symbol	Test Condition	Min	Typ.	Max	Unit
POWER SUPPLY (VM)						
VM operating voltage	VM		2.5		5.5	V
stand-by current	I _{DDST}	EN=0		0.1	1	μA
Low power mode current	I _{DDL}	GLPEN=1 (Low power register)		16	25	μA
Operating Current	I _{DD}	No load			4.5	mA
LOGIC-LEVEL INPUTS						
Input logic low voltage	V _{INL}	VDDIO=3.3V			0.9	V
Input logic high voltage	V _{INH}	VDDIO=3.3V	1.2			V
Input logic low voltage	V _{INL}	VDDIO=1.8V			0.6	V
Input logic high voltage	V _{INH}	VDDIO=1.8V	0.9			V
EN/SDI/SCLK pull-down resister	R _{s1}			100		kΩ
CS pull-up resister	R _{s2}			100		kΩ
SDO output logic low voltage	V _{OUTL}	I _{load} =5mA, VM=5V			0.3	V
SDO output logic high voltage	V _{OUTH}	I _{load} =5mA, VM=5V	VDD			V
SCLK/SDI operating frequency	F _{sclk}				5	MHz
PZT output speed	F _{pzt}			18		kHz
MOTOR DRIVER OUTPUTS (OUT1, OUT2)						
1~4CH on resistance	R _{ds(on)2}	I _{LOAD} =0.5A, HS_Pmos+LS_N MOS		1.3	1.6	Ω
5CH (IR Cut) on resistance	R _{ds(on)1}	I _{LOAD} =0.2A, HS_Pmos+LS_N MOS		2.4	3	Ω
Output dead time	t _{DEAD}			150		ns
PROTECTION CIRCUITS						
VM under voltage lockout	V _{UVLO}	VM rises until operation recovers			2.4	V
VM under voltage hysteresis	V _{UV_HYS}	Rising to falling threshold		150		mV
Thermal shutdown threshold (Note 3)	T _{SD}			168		°C
Thermal shutdown hysteresis (Note 3)	T _{HYS}			35		°C

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: T_J is calculated from the ambient temperature T_A and power dissipation P_D according to the following formula: $T_J = T_A + P_D \times \theta_{JA}$. The maximum allowable continuous power dissipation at any ambient temperature is calculated by $P_{D(MAX)} = (T_{J(MAX)} - T_A) / \theta_{JA}$.

Note 3: Thermal shutdown threshold and hysteresis are guaranteed by design.

OPERATION

Overview

The TMI8150B is a DC motor driver integrated chip that works under low input voltage with a total of 5 groups of drive output channels. Four groups of motor drive outputs control the X axis and Y axis of the PZT motor with independent quadrature SPWM. Each group of output channels is composed of 4 MOSFETs, and the internal resistance is 1.3Ω . One set of motor drive output ports control IR cut, and conduct internal resistance 2.4Ω . Using SPI control, it is more convenient and accurate to control the motor speed and move. The chip has an integrated low power consumption mode, which can be accessed through software or an enable signal, and the quiescent current is only $16\mu A$ in the low power consumption mode. A variety of protection functions can prevent the equipment against failure occurs.

PZT Control

The TMI8150B can control the X-axis and Y-axis of the PZT motor. When working in 'Start the control quadrature SPWM mode' or 'The number of steps controls the quadrature SPWM mode'. Start control quadrature SPWM mode can be called automatic control mode. It can continuously output SPWM control signal. Step control quadrature SPWM mode can be called manual control mode. It can work according to the set target number of turns and phase.

IR-Cut Control

The TMI8150B can control IR-Cut at the same time. It can support direction switching at any time. Three working states can be selected: Forward, Reverse and Brake.

Function description

The TMI8150B uses SPI communication mode to control the motor drive, which can set the target number of rotations, phase value, and PWM width of the motor, and record the motor rotation position in real time. Control the working mode of the motor: 1. Drive port direct control mode; 2. 1-2 phase excitation mode; 3. 2 Phase excitation mode; 4. SPI pulse width control mode; 5. Start the control quadrature SPWM mode; 6. The number of steps controls the quadrature SPWM mode (default). It supports forward and reverse rotation, and different step values can be set to control the motor speed.

VM Under-voltage Lockout (UVLO)

If at any time the voltage on the VM pin falls below the under-voltage-lockout threshold voltage, all FETs in the H-bridge will be disabled. Operation resumes when VM rises above the UVLO threshold.

Thermal Shutdown (TSD)

If the die temperature exceeds safe limits, all FETs in the H-bridge are disabled. After the die temperature has fallen to a safe level, operation automatically resumes.

Block Diagram

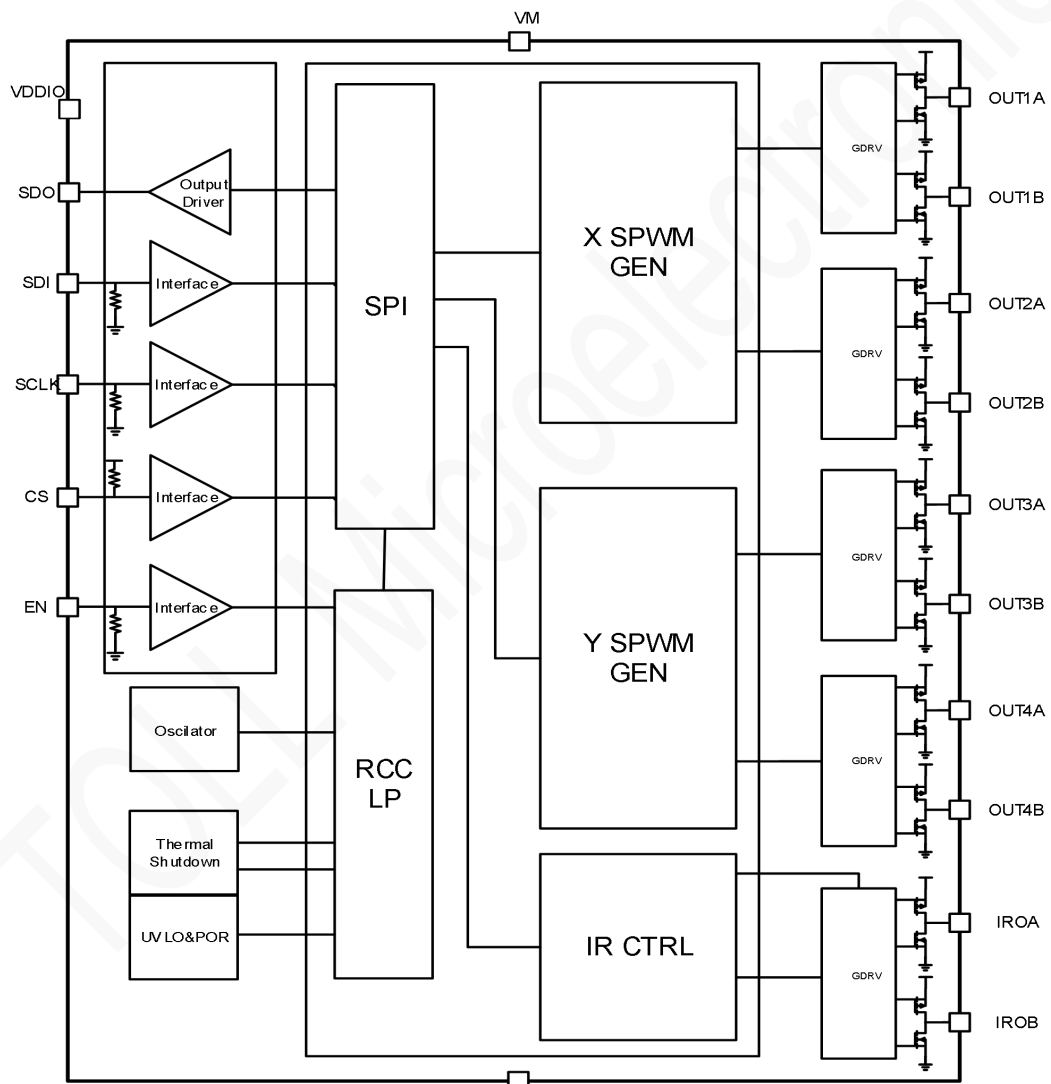
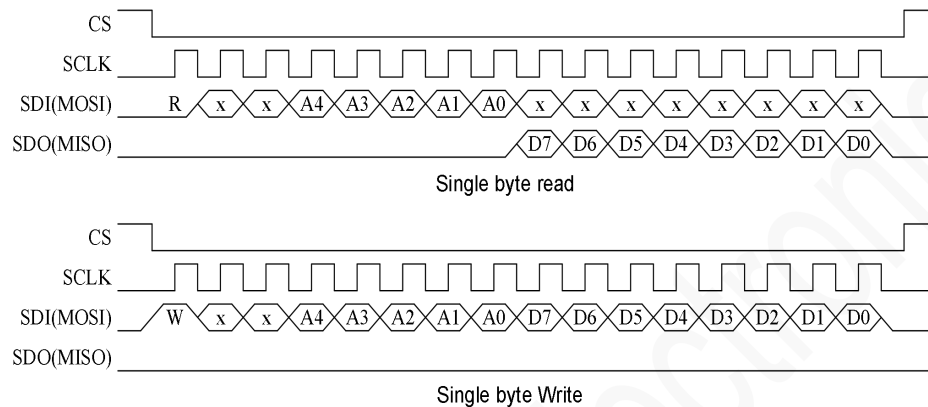


Figure 2. TMI8150B Block Diagram

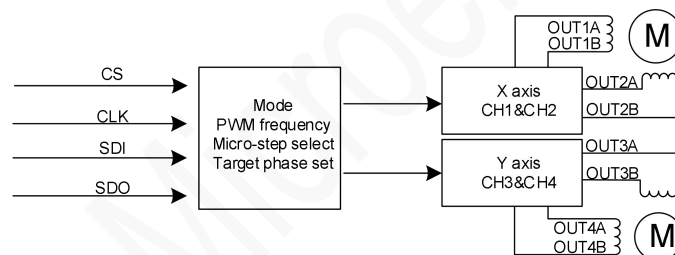
Logic control description

SPI Logic Description

The TMI8150B is controlled by SPI communication. By controlling the logic levels of CS, CLK, SDI, SDO, the output waveform of the chip can be controlled. CS is a logic control signal, active at low level. The maximum clock frequency is 5MHz. The logic control sequence of TMI8150B is as follows:



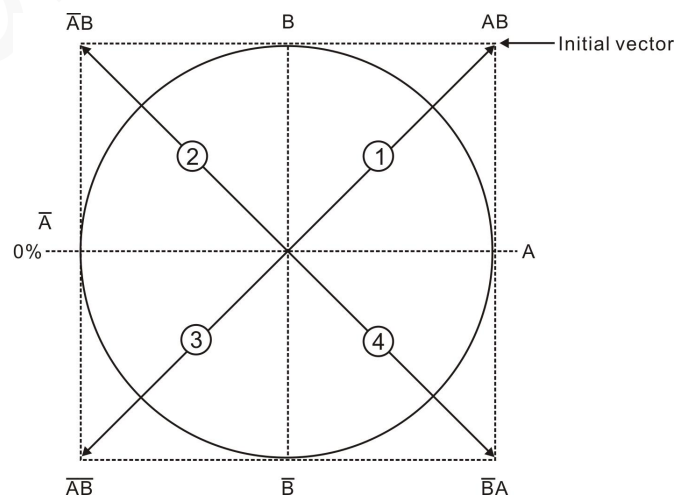
Stepper motor control



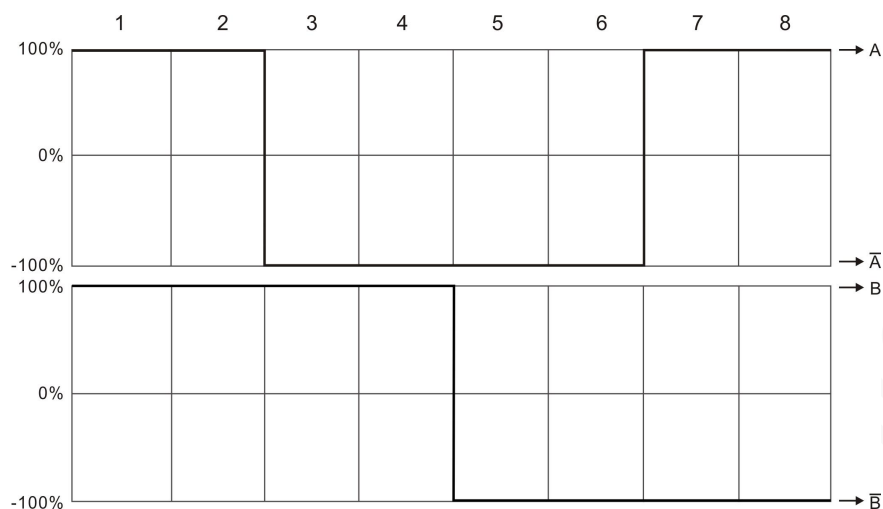
1~4CH Control mode

(1) 2 Phase

OUTPUT CURRENT VECTOR

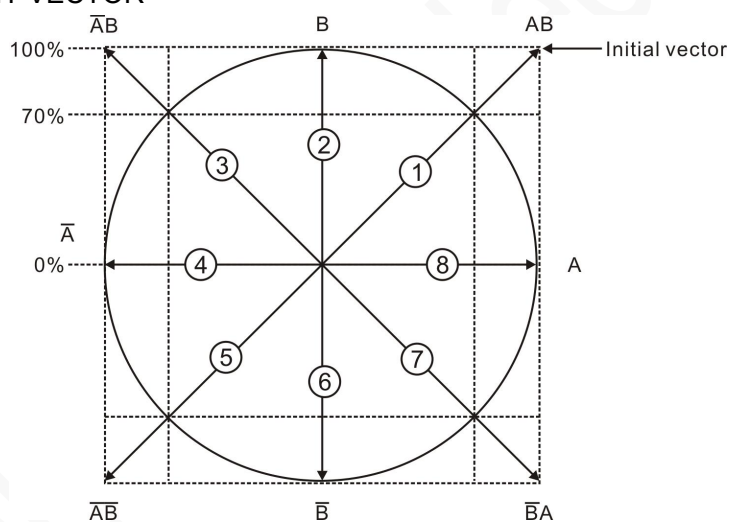


Output current waveform of 2-2 phase

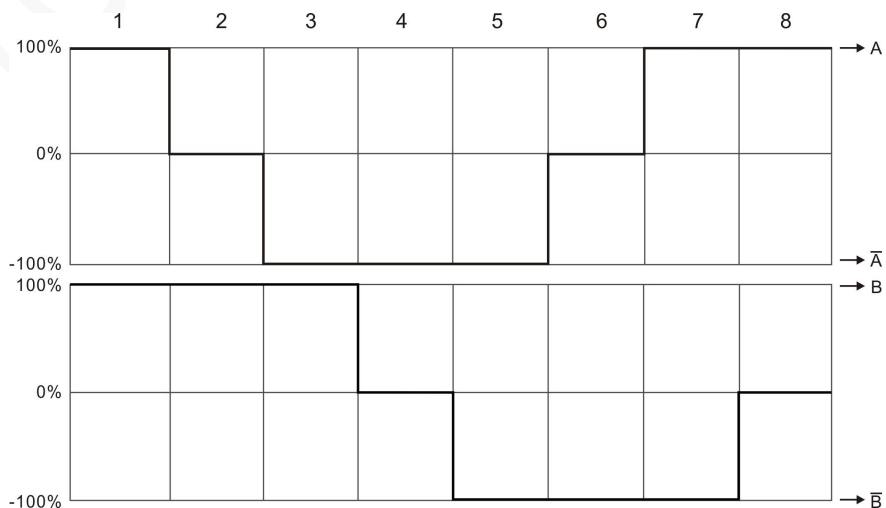


(2) 1-2 Phase

OUTPUT CURRENT VECTOR



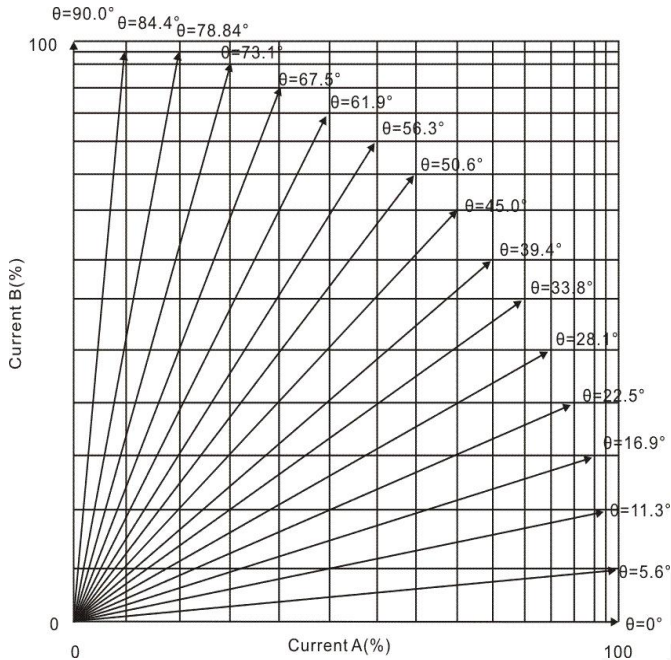
Output current waveform of 1-2 phase



(3) Micro Step Control

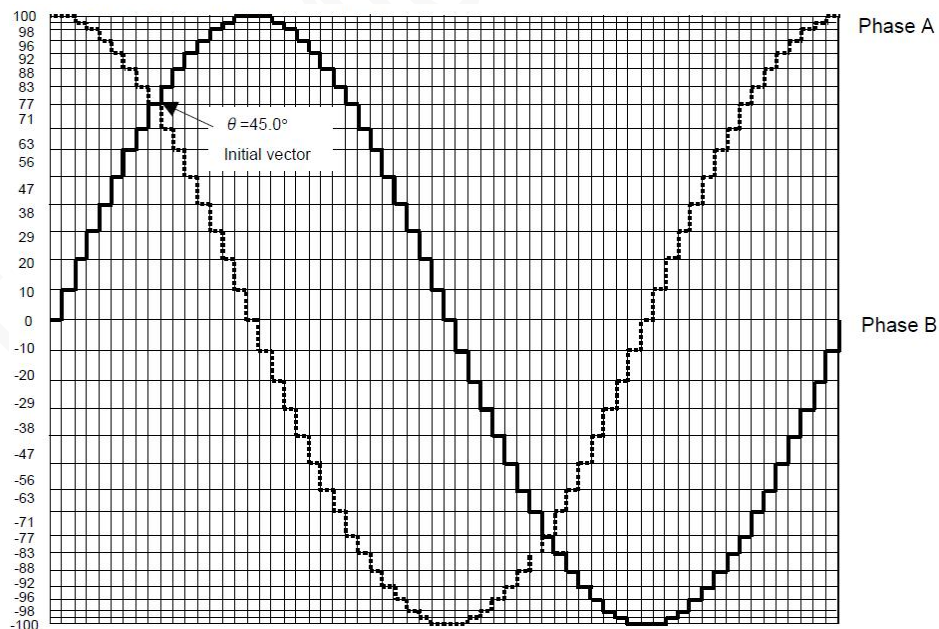
Any of the four resolutions are selectable: 64, 128, 256 and 512 steps. The example given below is for 64 steps:

OUTPUT CURRENT VECTOR



ACH & BCH POSITION AND CURRENT

Step	Set Angle	Current B	Current A
0	0.0	0.0%	100.0%
1	5.6	9.8%	100.0%
2	11.3	19.5%	98.1%
3	16.9	29.0%	95.7%
4	22.5	38.3%	92.4%
5	28.1	47.1%	88.2%
6	33.8	55.6%	83.1%
7	39.4	63.4%	77.3%
8	45.0	70.7%	70.7%
9	50.6	77.3%	63.4%
10	56.3	83.1%	55.6%
11	61.9	88.2%	47.1%
12	67.5	92.4%	38.3%
13	73.1	95.7%	29.0%
14	78.8	98.1%	19.5%
15	84.4	100.0%	9.8%
16	90.0	100.0%	0%



Output current waveform of pseudo sine wave

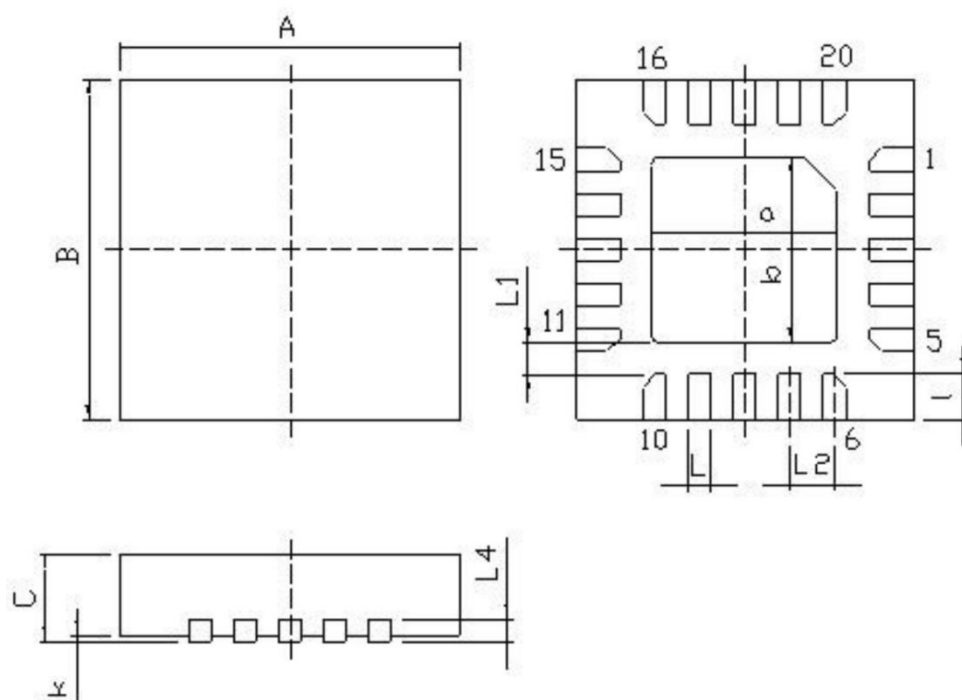
(4) IR cut Control

The TMI8150B can control IR cut to work in the following modes:

IREN	IRINA (register)	IRINB (register)	IROA(5 CH)	IROB(5 CH)	Status
H	H	L	H	L	Forwards
H	L	H	L	H	Backwards
H	H	H	L	L	Brake
H	L	L	High-Z	High-Z	Coast

PACKAGE INFORMATION

QFN3x3-20L



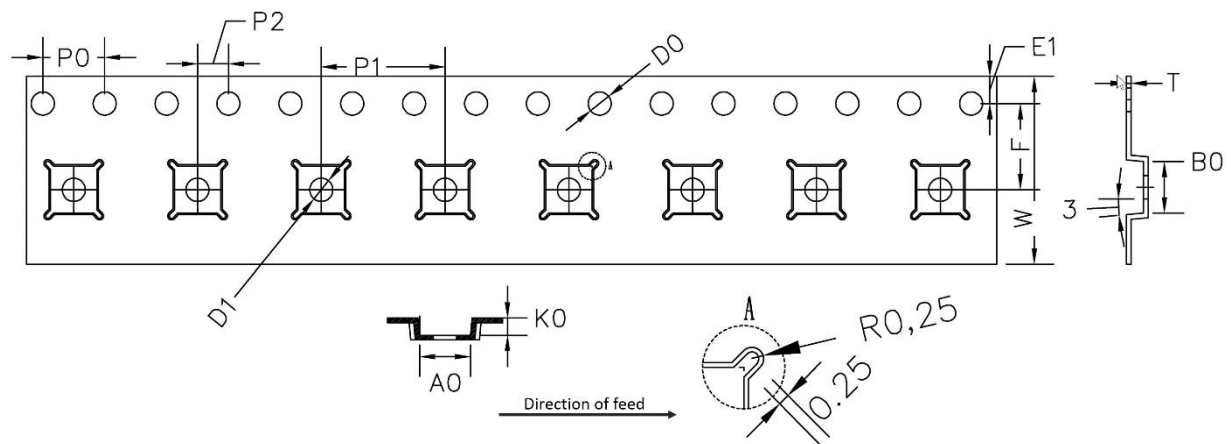
Symbol	Dimensions In Millimeters			Symbol	Dimensions In Inches		
	Min.	TYP.	Max.		Min.	TYP.	Max.
A	2.90	3.00	3.10	L2	-	0.40	-
B	2.90	3.00	3.10	L4	-	0.203	-
C	0.70	0.75	0.80	a	1.60	1.65	1.70
L	0.15	0.20	0.25	b	1.60	1.65	1.70
L1	0.20	-	-	k	0.00	0.02	0.05
I	0.30	0.40	0.50				

Note:

- 1) Package length does not include mold flash, protrusion or gate burr.
- 2) Package width does not include inter lead flash or protrusion.
- 3) Lead popularity (bottom of leads after forming) shall be 0.10 millimeters max.
- 4) Pin 1 is upper left pin when reading top mark from left to right.

TAPE AND REEL INFORMATION

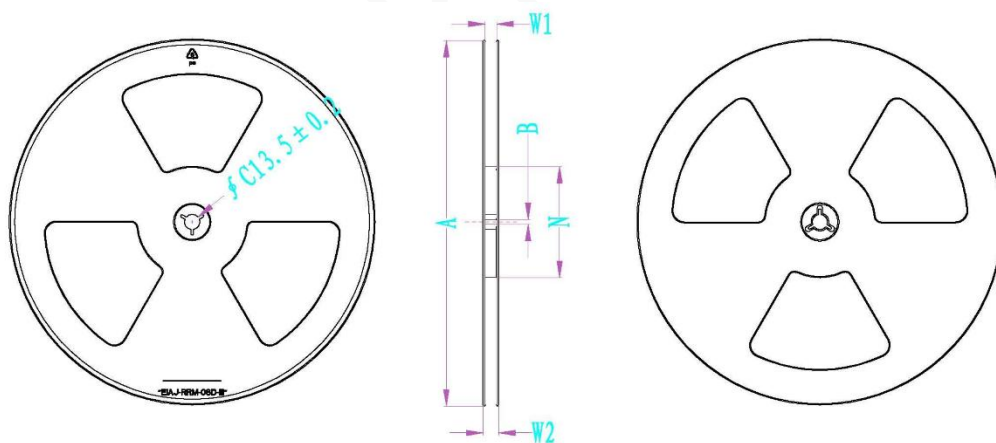
TAPE DIMENSIONS: QFN3x3-20L



Unit: mm

Symbol	Dimensions	Symbol	Dimensions	Symbol	Dimensions	Symbol	Dimensions
A0	3.30±0.10	P0	4.00±0.10	E1	1.75±0.10	D1	1.55±0.05
B0	3.30±0.10	P1	8.00±0.10	F	5.50±0.10	T	0.30±0.05
K0	1.10±0.10	P2	2.00±0.10	D0	1.55±0.05	W	12.00±0.30

REEL DIMENSIONS: QFN3x3-20L



Unit: mm

Ø A	B	Ø C	Ø N	W1	W2
330±1.0	4.7±0.5	13.5±0.2	100±0.5	13.4±0.5	17.4±0.5

Note:

- 1) All Dimensions are in Millimeter
- 2) Quantity of Units per Reel is 5000
- 3) MSL level is level 3.

Important Notification

This document only provides product information. Xi'an TOLL Microelectronic Inc. (TMI) reserves the right to make corrections, modifications, enhancements, improvements, and other changes to its products and to discontinue any product without notice at any time.

Xi'an TOLL Microelectronic Inc. (TMI) cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a TMI product. No circuit patent licenses are implied.

All rights are reserved by Xi'an TOLL Microelectronic Inc.

[http:// www.toll-semi.com](http://www.toll-semi.com)

TOLL Microelectronic