



TM05P04I

P-Channel Enhancement Mosfet

General Description

- Low $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

Applications

- Load switch
- PWM

General Features

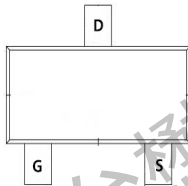
$V_{DS} = -40V, I_D = -4.8A$

$R_{DS(ON)} = 47m\Omega$ (Typ.) @ $V_{GS} = -10V$

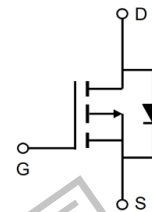
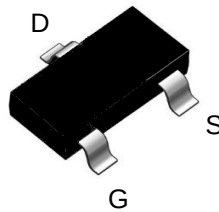
100% UIS Tested
100% R_g Tested



I: SOT-23



Marking: 5P04



Absolute Maximum Ratings $T_A = 25^\circ C$ un (less otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current	-4.8	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current	-3.6	A
I_{DM}	Pulsed Drain Current ²	-22	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation ³	2.0	W
$P_D @ T_A = 70^\circ C$	Total Power Dissipation ³	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	65	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹ ($t \leq 10s$)	---	48	$^\circ C/W$

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Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D= -250\mu A$	-40	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}= -40V, V_{GS}=0V$	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}= \pm 20V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D= -250\mu A$	-1.0	-1.7	-2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance Note2	$V_{GS}= -10V, I_D= -5A$	-	47	52	m Ω
		$V_{GS}= -4.5V, I_D= -4A$	-	53	69	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}= -20V, V_{GS}=0V,$ $f=1.0MHz$	-	869	-	pF
C_{oss}	Output Capacitance		-	94	-	pF
C_{rss}	Reverse Transfer Capacitance		-	69	-	pF
Q_g	Total Gate Charge	$V_{DS}= -20V, I_D= -4A,$ $V_{GS}= -10V$	-	17.3	-	nC
Q_{gs}	Gate-Source Charge		-	3.2	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	4.3	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}= -20V, I_D= -4A,$ $V_{GS}= -10V, R_{GEN}=3\Omega$	-	10.3	-	ns
t_r	Turn-on Rise Time		-	4.3	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	39	-	ns
t_f	Turn-off Fall Time		-	46.5	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	- 4.8	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-22	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S= -5.5A$	-	-0.8	-1.2	V
t_{rr}	Reverse Recovery Time	$V_{GS}=0V, I_S= -5.5A,$	-	17	-	ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s$	-	11.5	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

 2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

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Typical Performance Characteristics

Figure1: Output Characteristics

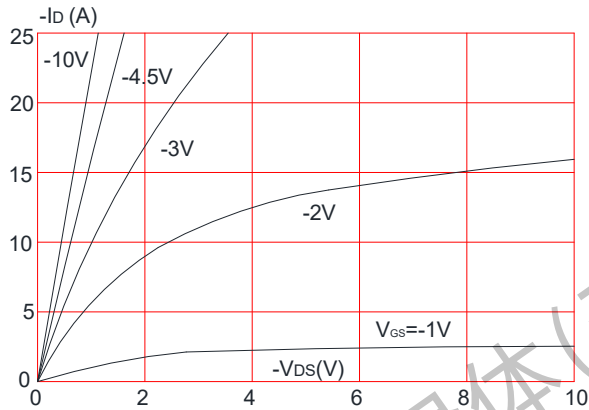


Figure 2: Typical Transfer Characteristics

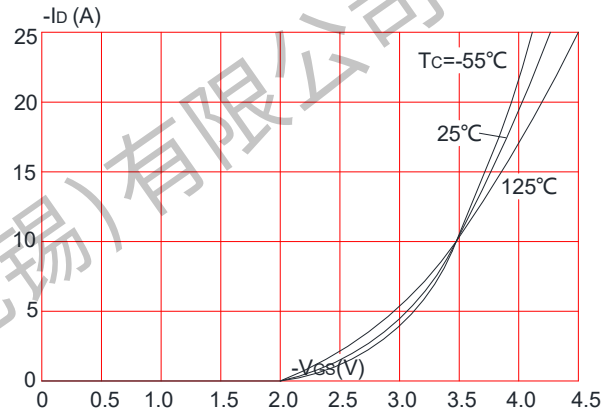


Figure 3: On-resistance vs. Drain Current

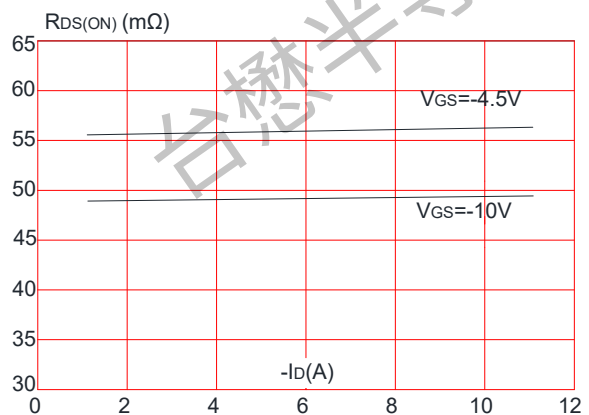


Figure 4: Body Diode Characteristics

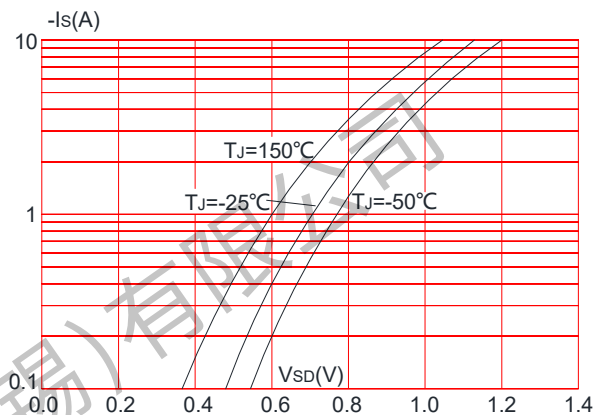


Figure 5: Gate Charge Characteristics

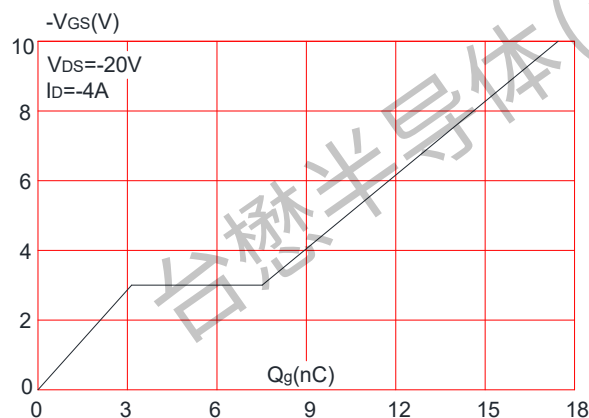
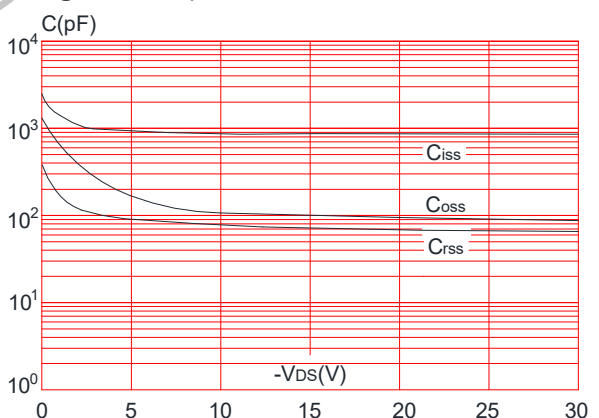


Figure 6: Capacitance Characteristics



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Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

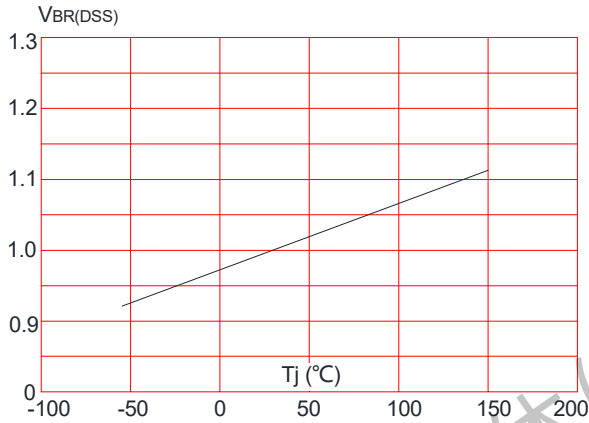


Figure 8: Normalized on Resistance vs. Junction Temperature

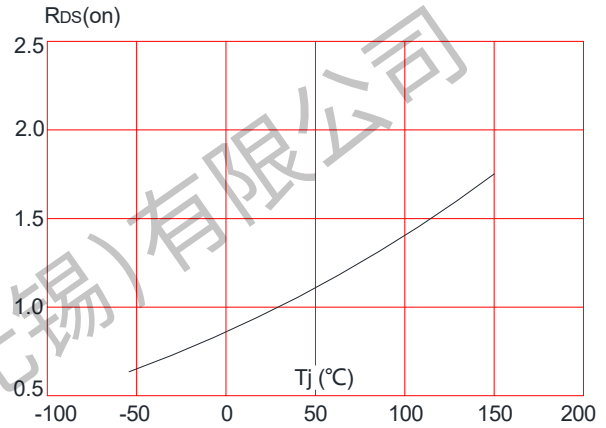


Figure 9: Maximum Safe Operating Area

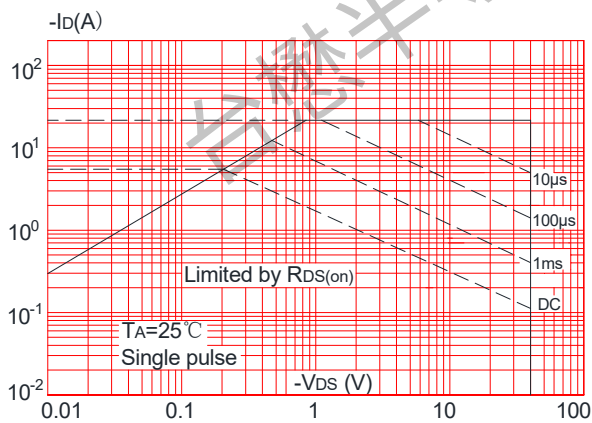


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

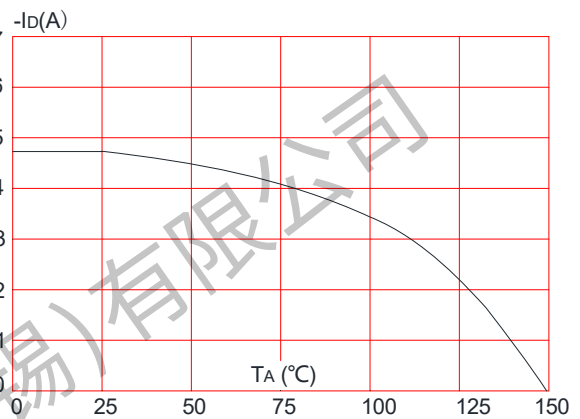
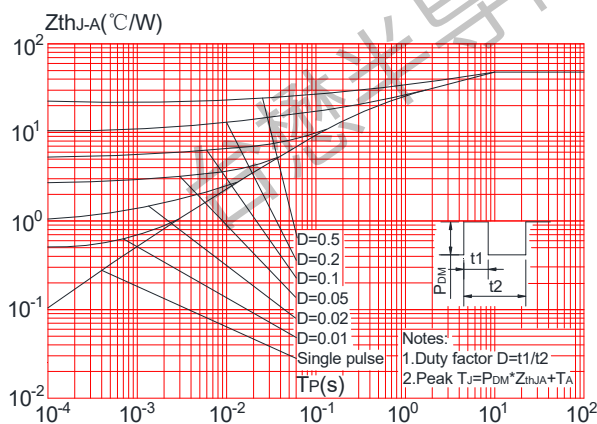


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

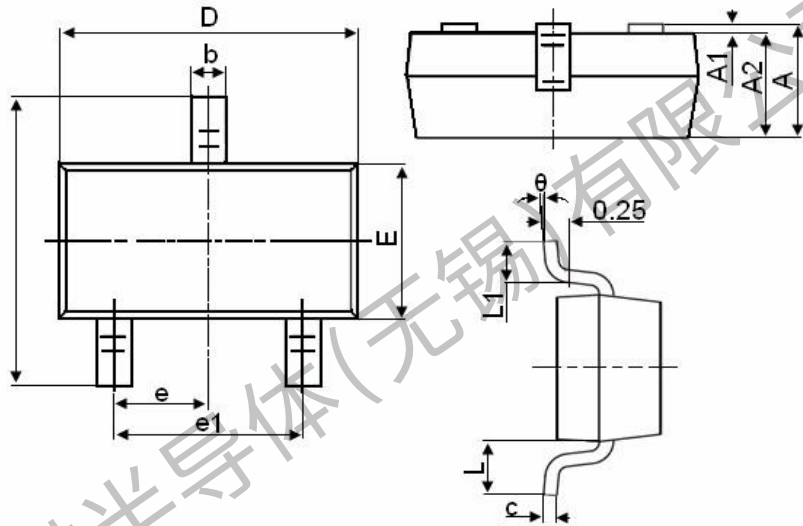




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Package Mechanical Data:SOT-23



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

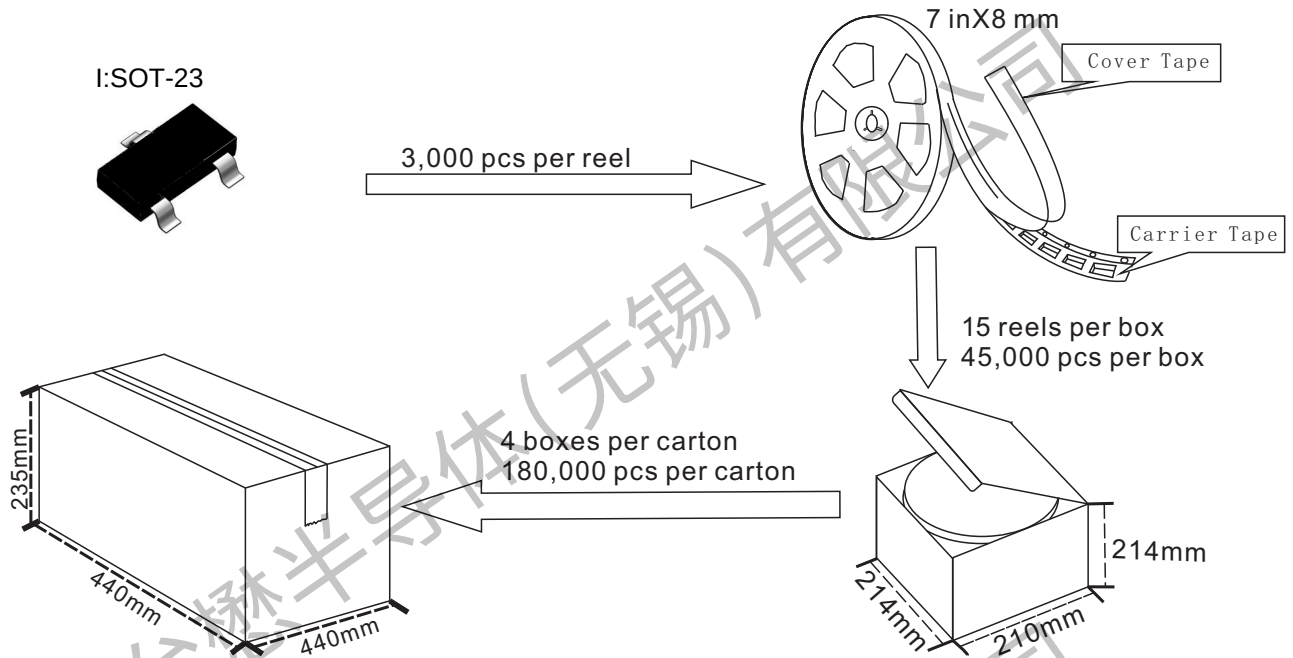


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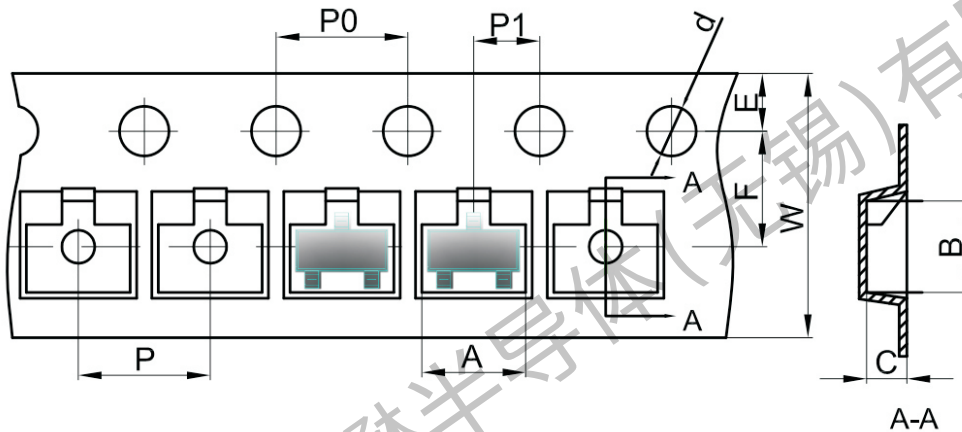
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SOT-23 Packing

1. The method of packaging and dimension are shown as below figure. (Dimension in mm)



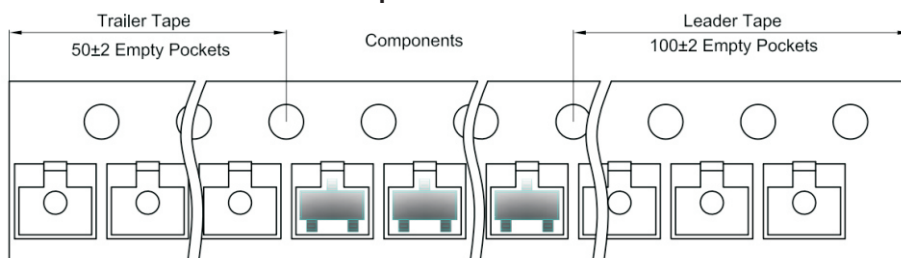
SOT-23 Embossed Carrier Tape



Dimensions are in millimeter

Pkg type	A	B	C	d	E	F	P0	P	P1	W
SOT-23	3.15	2.77	1.22	Ø1.50	1.75	3.50	4.00	4.00	2.00	8.00

SOT-23 Tape Leader and Trailer



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Revision history:

Date	Rev	Description	Page
2023.08.11	23.08	Original	