

N-Channel 60V MOSFET

E060N3P5DH1

V _{DS} (V)	$R_{DS(on),max}$ (m Ω)	I _D (A)
60V	3.5 @ V _{GS} = 10V	130

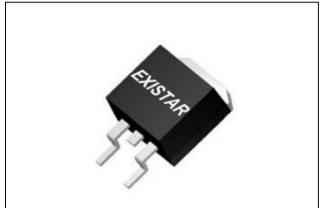
Features

- Low R_{DS(on)} trench technology
- Low thermal impedance
- Fast switching speed
- 100% avalanche tested

Applications

- DC/DC conversion
- Power switch
- PD charger
- Moto driver

TO-263





Package And Ordering Information

Ordering code	Package	Marking
E060N3P5DH1	TO-263	E060N3P5DH1

Ordering Information

Package	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box
TO-263	50	16	800



Key Performance Parameters

Parameter	Value	Unit
VDS, min @ Tj(max)	60	V
ID, pulse	390	А
RDS(ON), max @ VGS=10V	3.5	mΩ
Qg	56.8	nC

Absolute Maximum Ratings at Tj=25°C Unless Otherwise Noted

Parameter		Symbol	Limit	Unit
Drain-source voltage	V _{DS}	60		
Gate-source voltage		V _{GS}	±20	V
	T _C =25°C		130	
Continuous drain current	T _C =100°C	- I _D	-	
Pulsed drain current	I _{D,pulse}	390	А	
Avalanche energy, single pulse	E _{AS}	94	mJ	
Power dissipation	T _C =25°C		140	
rowei uissipalioti	T _A =25°C	P_{D}	-	W
Operating junction and storage temperature range	T _J , T _{stg}	-55 to 150	°C	

Thermal Characteristics

Parameter		Symbol	Max.	Uni t
Thermal resistance, junction-to-case	Steady state	R _{eJC}	0.89	
Thermal resistance, junction-to-ambient	Steady state	Reja	62	°C/W

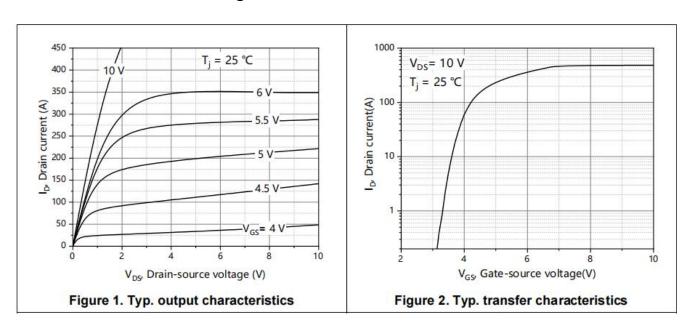
Electrical Characteristics at Tj=25°C unless otherwise specified

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions	
Static							
Drain to source breakdown voltage	V _{(BR)DSS}	60			>	V _{GS} = 0, I _D = 250 μA	
Gate-source threshold voltage	V _{GS} (th)	2	2.75	3.75	٧	V _{DS} = V _{GS} , I _D = 250 μA	
Gate-body leakage	I _{GSS}			±100	nA	V _{DS} = 0 V, V _{GS} = ±20 V	
Zero gate voltage drain current	I _{DSS}			1	μΑ	V _{DS} = 60 V, V _{GS} = 0 V	
Drain-source on-resistance	Ros(on)		2.5	3.5	mΩ	V _{GS} = 10 V, I _D = 20 A	
Forward transconductance	gfs		-		S	V _{DS} = 5 V, I _D = 30 A	
Gate resistance	Rg		2.5		Ω	f=1MHz	

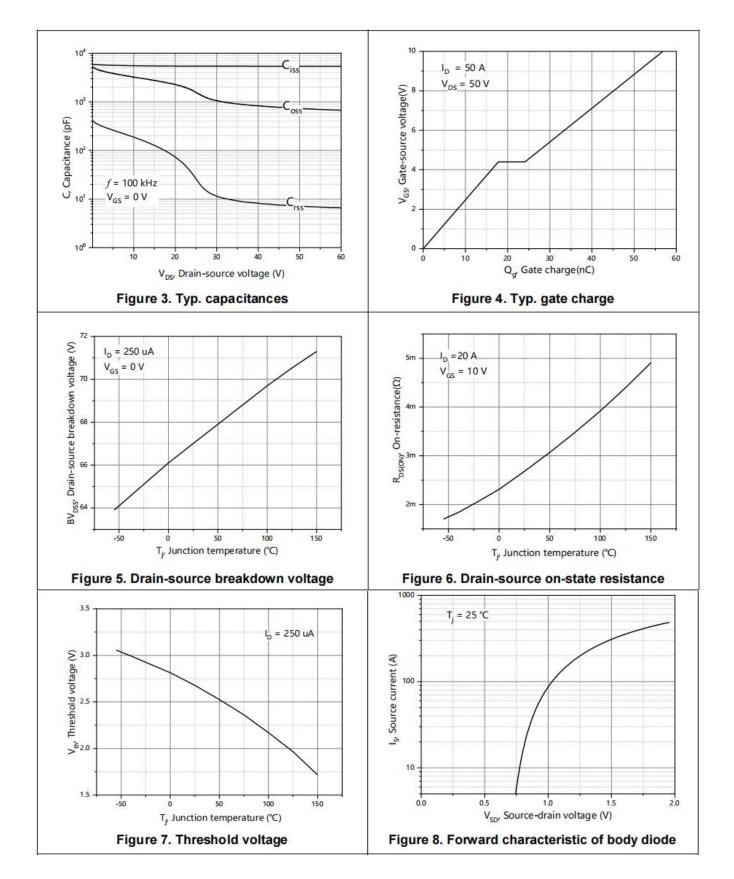


Gate Charge							
Total gate charge	Qg		56.8				
Gate-source charge	Qgs		17.8		nC	V _{DS} = 50 V, I _D = 50 A, V _{GS} = 10 V	
Gate-drain charge	Qgd		6.3				
			ynamic	;			
Turn-on delay time	t _{d(on)}		31.4				
Rise time	t _r		54.8			V _{DS} = 50 V, I _D =50 A, V _{GS} = 10 V,	
Turn-off delay time	$t_{d(off)}$		60.5		ns	$R_{GEN} = 2 \Omega$	
Fall time	t _f		112.5		113		
Input capacitance	C _{iss}		5411				
Output capacitance	C _{oss}		1522			V _{DS} =25 V, V _{GS} = 0 V, f = 100kHz	
Reverse transfer capacitance	C _{rss}		24.2		pF		
Body Diode							
Diode forward voltage	V_{SD}			1.3	V	V _{GS} = 0 V, I _F = 20 A	
Reverse recovery time	t _{rr}		70		ns	V _R = 50 V, I _S =50 A, di/dt = 100	
Reverse recovery charge	Qrr		95		nC	A/µs	

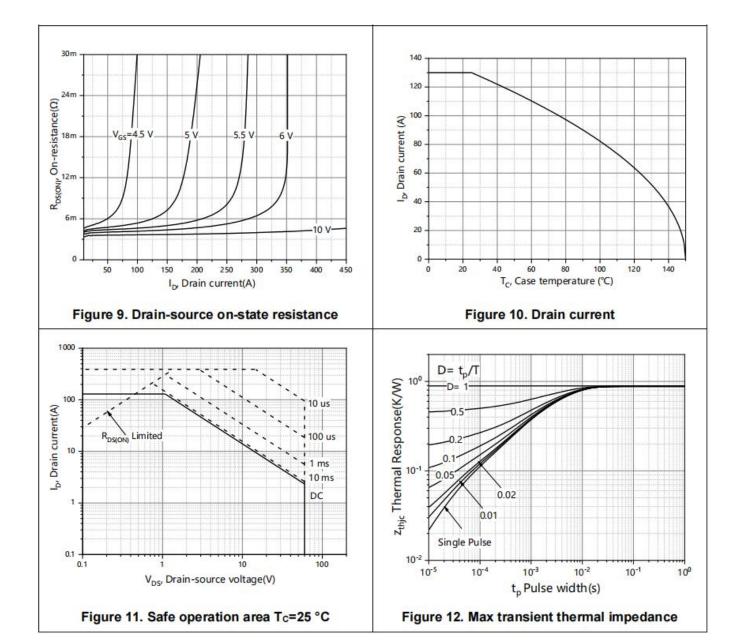
Electrical Characteristics Diagrams













Test circuits and waveforms

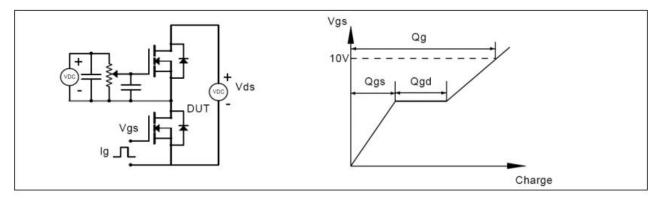


Figure 1. Gate charge test circuit & waveform

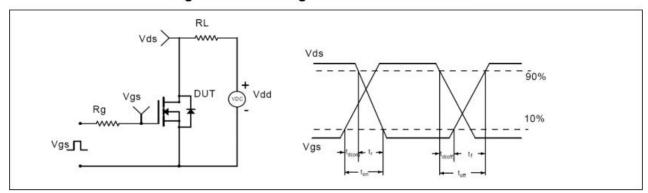


Figure 2. Switching time test circuit & waveforms

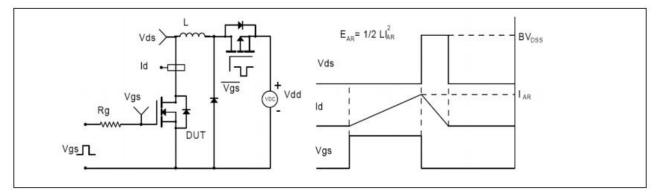


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

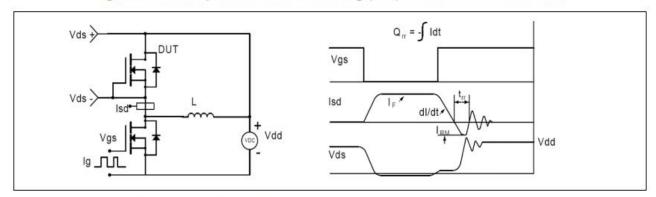
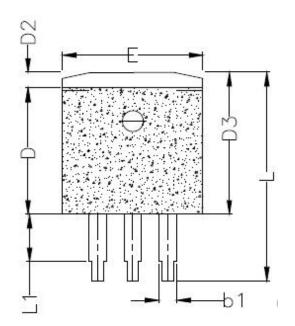


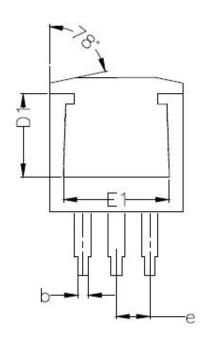
Figure 4. Diode reverse recovery test circuit & waveforms

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Package Outline Dimensions





SYMBOL	MIN	NOM	MAX			
Α	4.40	4.57	4.70			
A1	2.59	2.69	2.79			
A2	1.40	1.50	1.60			
b	0.71	0.81	0.91			
b1	1.17	1.27	1.37			
С	1.17	1.27	1.37			
c1	0.30	0.38	0.45			
D	9.00	9.15	9.30			
D1	6.10	6.30	6.50			
D2	0.91	1.11	1.31			
D3	10.06	10.26	10.46			
E	10.00	10.16	10.40			
E1	7.92	8.02	8.12			
L	14.90	15.10	15.40			
L1	3.23	3.43	3.63			
L2	2.20	2.30	2.40			
е	2.54BSC					
θ	0.	()	8°			
θ 1	3°	5	8°			



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