

FMV20N60S1

FUJI POWER MOSFET

Super J-MOS series

N-Channel enhancement mode power MOSFET

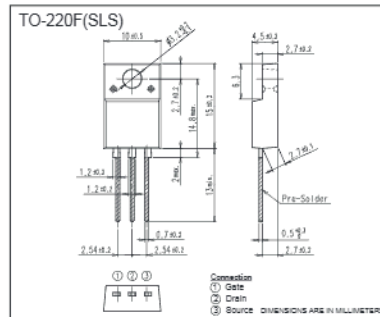
■ Features

- Low on-state resistance
- Low switching loss
- easy to use (more controllable switching dV/dt by R_g)

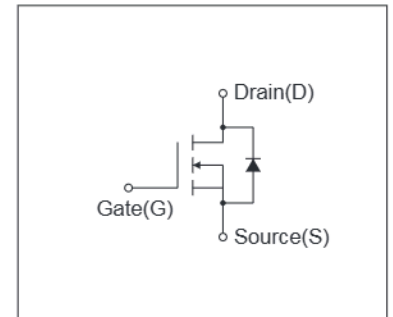
■ Applications

- UPS
- Server
- Telecom
- Power conditioner system
- Power supply

■ Outline Drawings [mm]



■ Equivalent circuit schematic



■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings at $T_c=25^\circ\text{C}$ (unless otherwise specified)

Description	Symbol	Characteristics	Unit	Remarks
Drain-Source Voltage	V_{DS}	600	V	
	V_{DSX}	600	V	$V_{GS}=-30\text{V}$
Continuous Drain Current	I_D	± 20	A	$T_c=25^\circ\text{C}$ Note*1
		± 12.6	A	$T_c=100^\circ\text{C}$ Note*1
Pulsed Drain Current	I_{DP}	± 60	A	
Gate-Source Voltage	V_{GS}	± 30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	I_{AR}	6.6	A	Note *2
Non-Repetitive Maximum Avalanche Energy	E_{AS}	472.2	mJ	Note *3
Maximum Drain-Source dV/dt	dV_{DS}/dt	50	kV/ μs	$V_{DS} \leq 600\text{V}$
Peak Diode Recovery dV/dt	dV/dt	15	kV/ μs	Note *4
Peak Diode Recovery $-di/dt$	$-di/dt$	100	A/ μs	Note *5
Maximum Power Dissipation	P_o	2.16	W	$T_a=25^\circ\text{C}$
		53		$T_c=25^\circ\text{C}$
Operating and Storage Temperature range	T_{ch}	150	$^\circ\text{C}$	
	T_{stg}	-55 to +150	$^\circ\text{C}$	
Isolation Voltage	V_{iso}	2	kVrms	$t=60\text{sec}, f=60\text{Hz}$

Note *1 : Limited by maximum channel temperature.

Note *2 : $T_{ch} \leq 150^\circ\text{C}$, See Fig.1 and Fig.2

Note *3 : Starting $T_{ch}=25^\circ\text{C}$, $I_{AS}=2\text{A}$, $L=216\text{mH}$, $V_{DS}=60\text{V}$, $R_g=50\Omega$, See Fig.1 and Fig.2

E_{AS} limited by maximum channel temperature and avalanche current.

Note *4 : $I_f \leq -I_o$, $-di/dt=100\text{A}/\mu\text{s}$, $V_{DS} \leq 400\text{V}$, $T_{ch} \leq 150^\circ\text{C}$.

Note *5 : $I_f \leq -I_o$, $dV/dt=15\text{kV}/\mu\text{s}$, $V_{DS} \leq 400\text{V}$, $T_{ch} \leq 150^\circ\text{C}$.

● Electrical Characteristics at $T_c=25^\circ\text{C}$ (unless otherwise specified)
Static Ratings

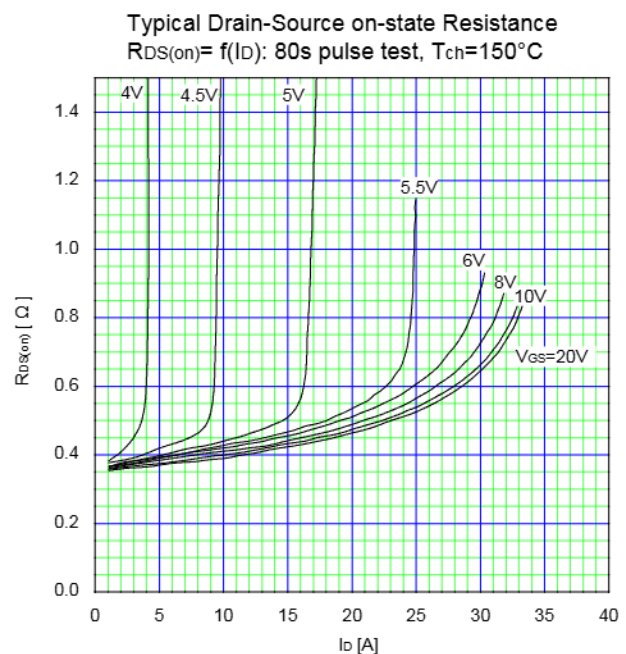
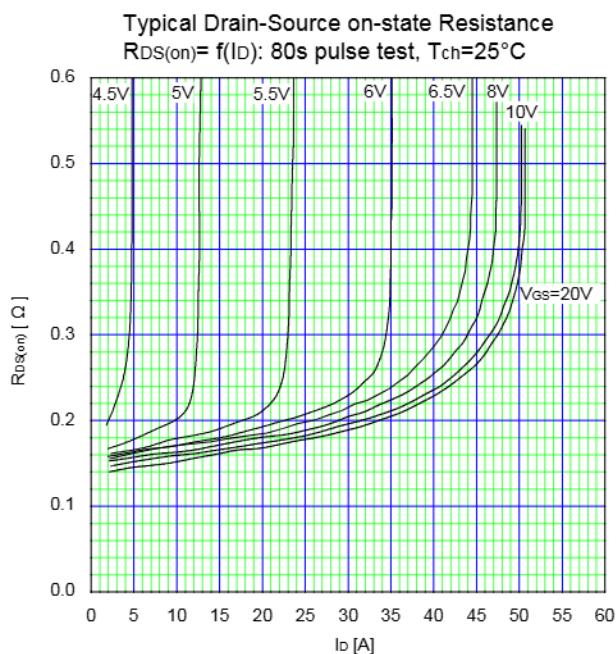
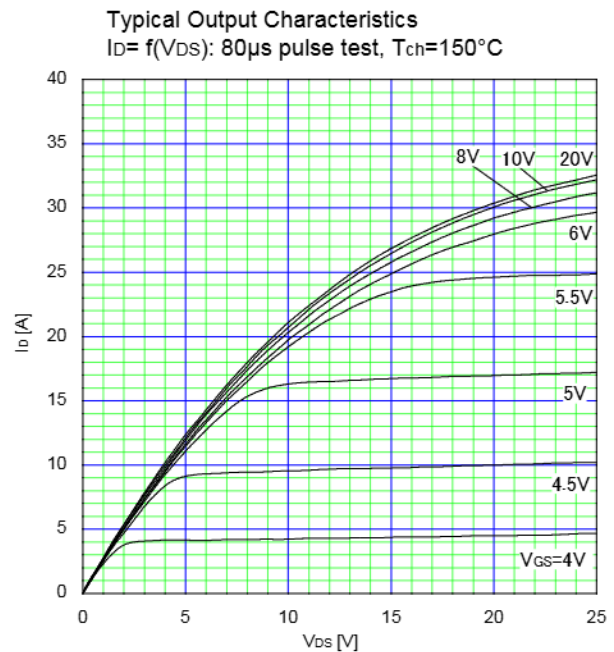
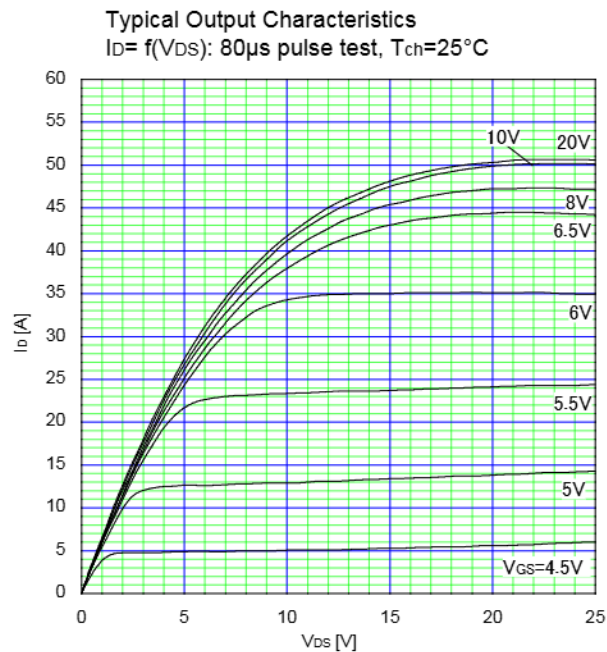
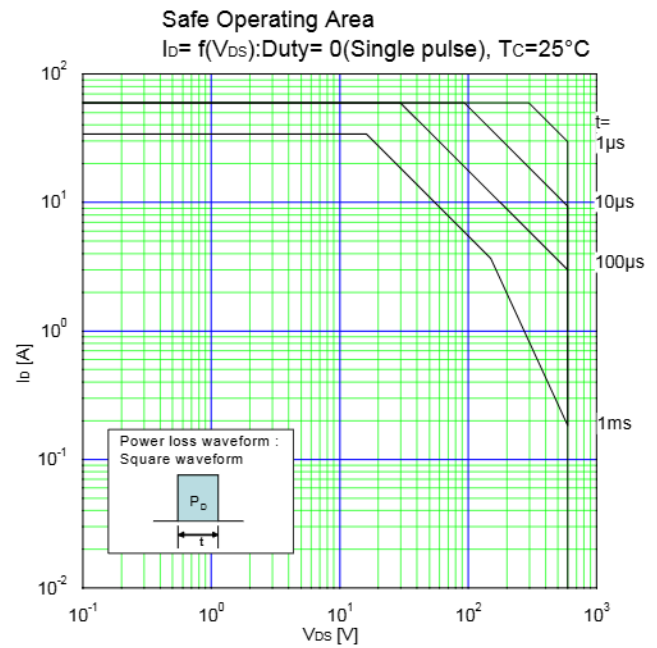
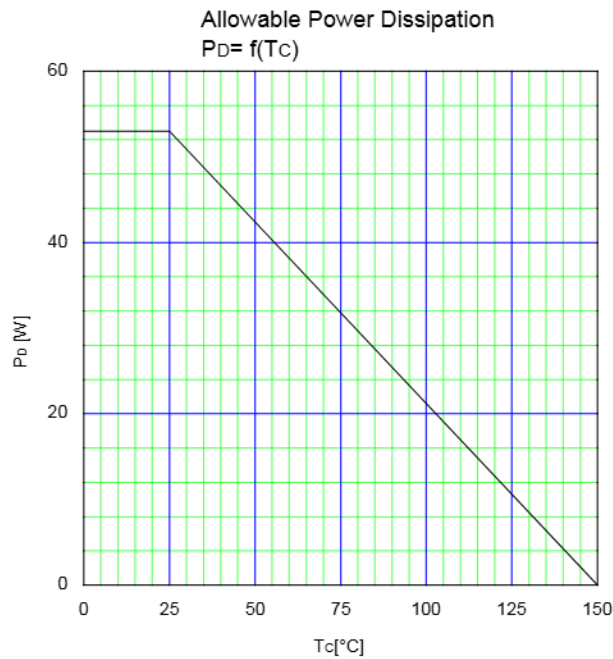
Description	Symbol	Conditions	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}$ $V_{GS}=0\text{V}$	600	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$I_D=250\mu\text{A}$ $V_{DS}=V_{GS}$	2.5	3	3.5	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=600\text{V}$ $V_{GS}=0\text{V}$ $T_{ch}=25^\circ\text{C}$	-	-	25	μA
		$V_{DS}=480\text{V}$ $V_{GS}=0\text{V}$ $T_{ch}=125^\circ\text{C}$	-	-	250	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30\text{V}$ $V_{DS}=0\text{V}$	-	10	100	nA
Drain-Source On-State Resistance	$R_{DS(on)}$	$I_D=10\text{A}$ $V_{GS}=10\text{V}$	-	0.161	0.19	Ω
Gate resistance	R_G	$f=1\text{MHz}$, open drain	-	3.7	-	Ω
Forward Transconductance	g_{fs}	$I_D=10\text{A}$ $V_{DS}=25\text{V}$	8.5	17.5	-	S
Input Capacitance	C_{iss}	$V_{DS}=10\text{V}$	-	1470	-	pF
Output Capacitance	C_{oss}	$V_{GS}=0\text{V}$	-	3120	-	
Reverse Transfer Capacitance	C_{rss}	$f=1\text{MHz}$	-	280	-	
Effective output capacitance, energy related (Note *6)	$C_{o(er)}$	$V_{GS}=0\text{V}$ $V_{DS}=0\ldots 480\text{V}$	-	90	-	
Effective output capacitance, time related (Note *7)	$C_{o(tr)}$	$V_{GS}=0\text{V}$ $V_{DS}=0\ldots 480\text{V}$ $ID=\text{constant}$	-	305	-	
Turn-On Time	$t_{d(on)}$	$V_{DD}=400\text{V}$, $V_{GS}=10\text{V}$ $I_D=10\text{A}$, $R_G=27\Omega$ See Fig.3 and Fig.4	-	22	-	ns
	t_r		-	40	-	
Turn-Off Time	$t_{d(off)}$		-	162	-	
	t_f		-	22	-	
Total Gate Charge	Q_G	$V_{DD}=480\text{V}$, $I_D=20\text{A}$ $V_{GS}=10\text{V}$ See Fig.5	-	48	-	nC
Gate-Source Charge	Q_{GS}		-	12.5	-	
Gate-Drain Charge	Q_{GD}		-	15	-	
Drain-Source crossover Charge	Q_{SW}		-	8	-	
Avalanche Capability	I_{AV}	$L=6.02\text{mH}$, $T_{ch}=25^\circ\text{C}$ See Fig.1 and Fig.2	6.6	-	-	A
Diode Forward On-Voltage	V_{SD}	$I_F=20\text{A}$, $V_{GS}=0\text{V}$ $T_{ch}=25^\circ\text{C}$	-	0.9	1.35	V
Reverse Recovery Time	t_{rr}	$I_F=20\text{A}$, $V_{GS}=0\text{V}$		370	-	ns
Reverse Recovery Charge	Q_{rr}	$V_{DD}=400\text{V}$ $-di/dt=100\text{A}/\mu\text{s}$, $T_{ch}=25^\circ\text{C}$	-	6.2	-	μC
Peak Reverse Recovery Current	I_{rp}	See Fig.6	-	32	-	A

Note *6 : $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 80% BV_{DSS} .

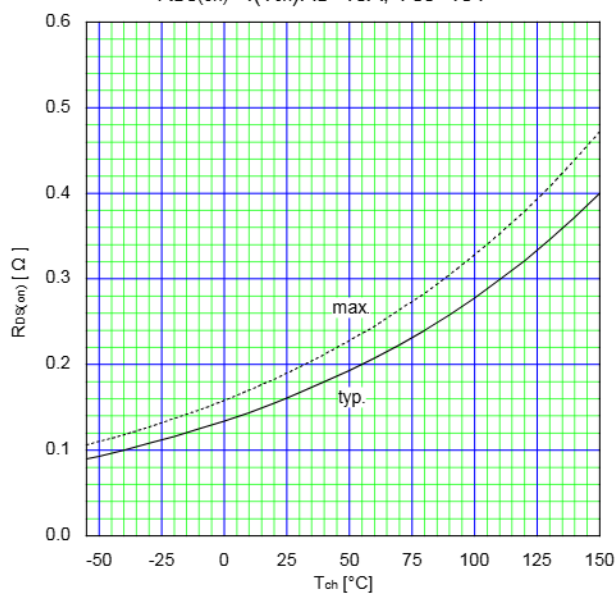
Note *7 : $C_{o(tr)}$ is a fixed capacitance that gives the same charging times as C_{oss} while V_{DS} is rising from 0 to 80% BV_{DSS} .

● Thermal Characteristics

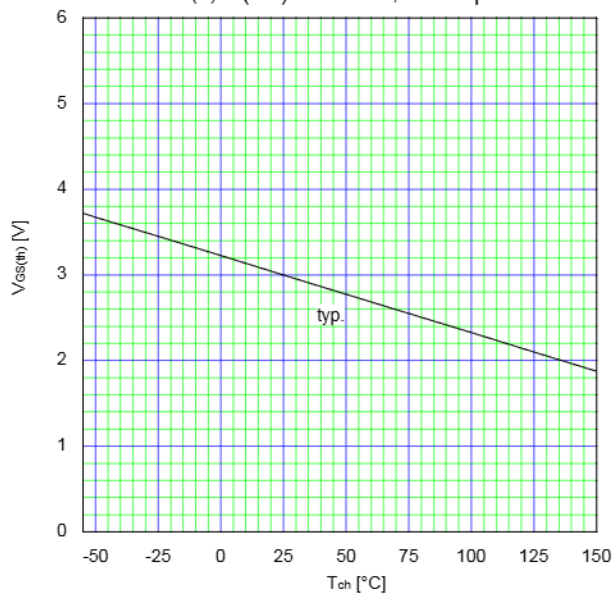
Description	Symbol	min.	typ.	max.	Unit
Channel to Case	$R_{th(ch-c)}$			2.36	$^\circ\text{C}/\text{W}$
Channel to Ambient	$R_{th(ch-a)}$			58	$^\circ\text{C}/\text{W}$



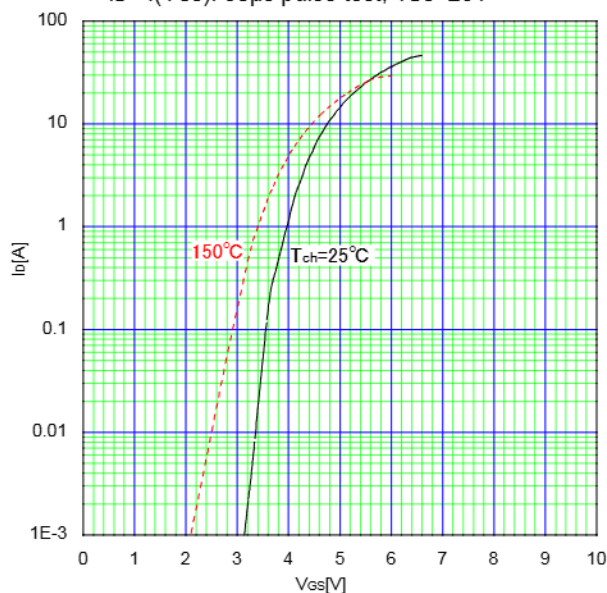
Drain-Source On-state Resistance
 $R_{DS(on)} = f(T_{ch})$: $I_D=10A$, $V_{GS}=10V$



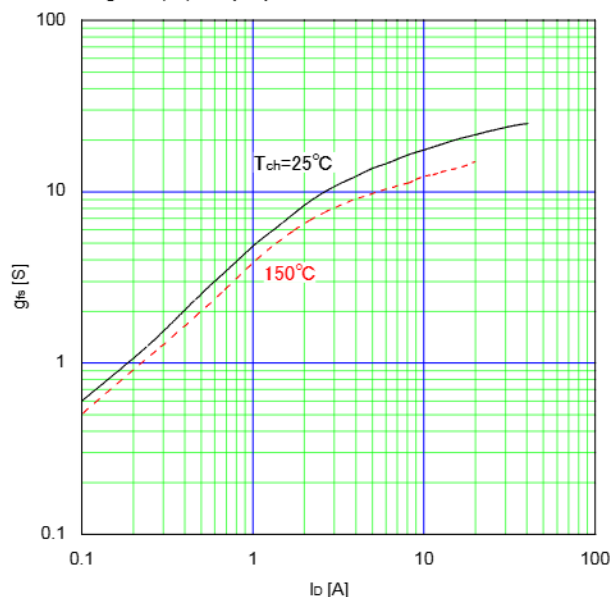
Gate Threshold Voltage vs. T_{ch}
 $V_{GS(th)} = f(T_{ch})$: $V_{DS}=V_{GS}$, $I_D=250\mu A$



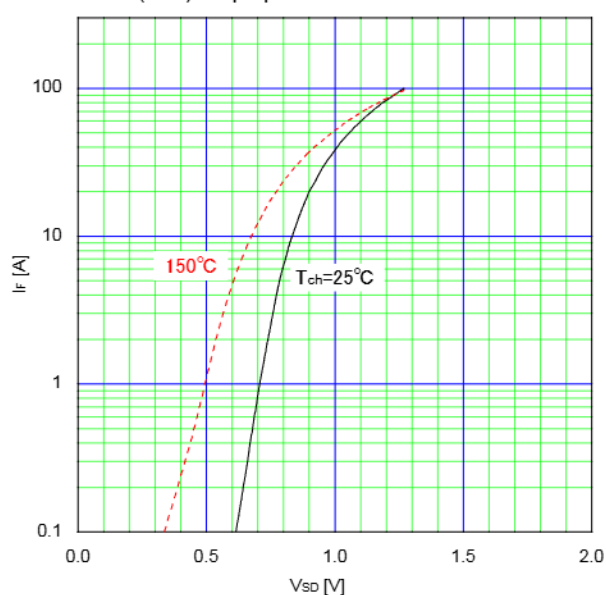
Typical Transfer Characteristic
 $I_D = f(V_{GS})$: 80μs pulse test, $V_{DS}=25V$



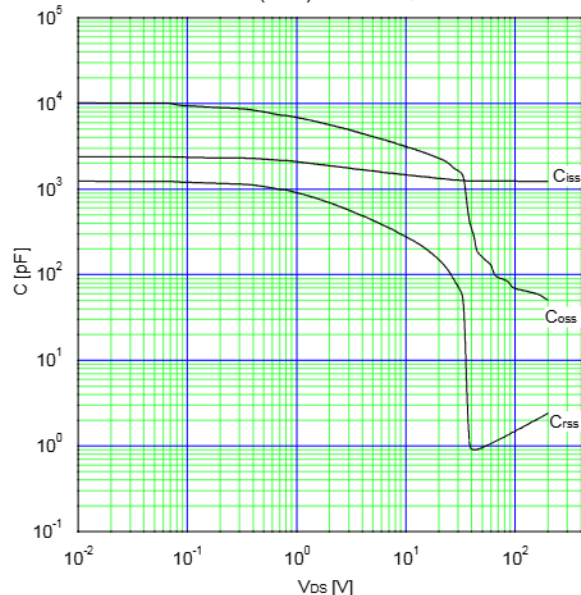
Typical Transconductance
 $g_{fs} = f(I_D)$: 80μs pulse test, $V_{DS}=25V$



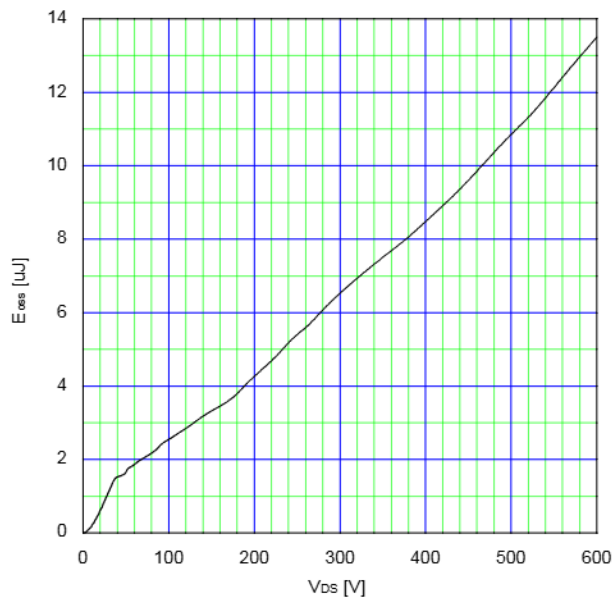
Typical Forward Characteristics of Reverse Diode
 $I_F = f(V_{SD})$: 80μs pulse test



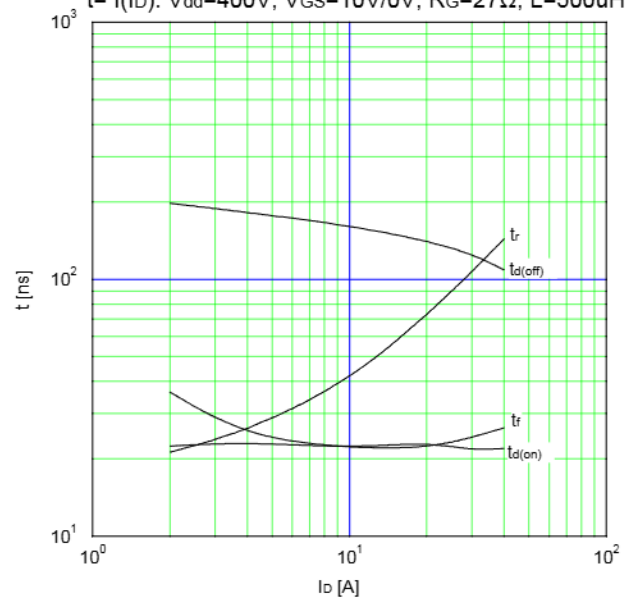
Typical Capacitance
 $C = f(V_{DS})$: $V_{GS}=0V$, $f=1MHz$



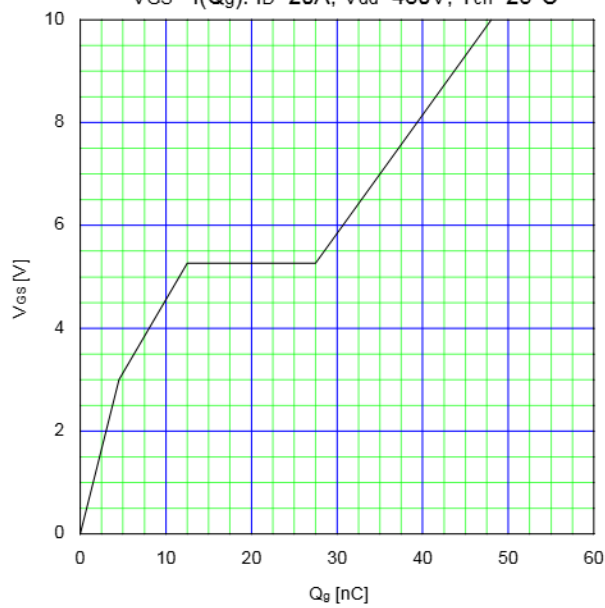
Typical Coss stored energy



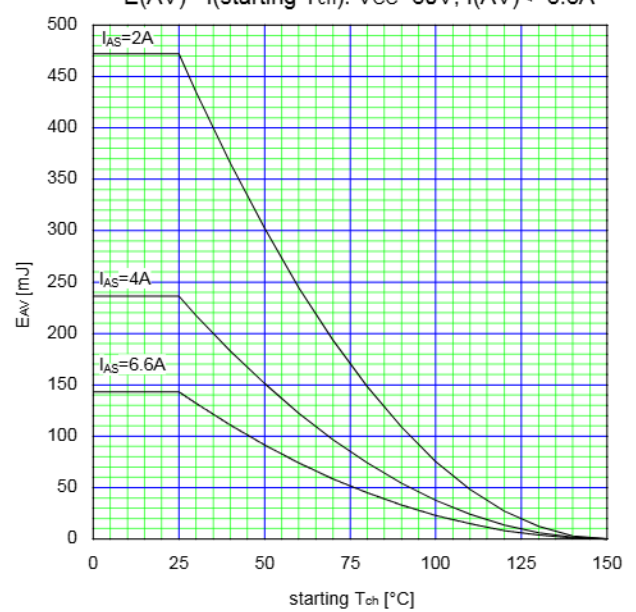
Typical Switching Characteristics vs. I_D $T_{ch}=25^\circ C$
 $t = f(I_D)$: $V_{dd}=400V$, $V_{GS}=10V/0V$, $R_G=27\Omega$, $L=500\mu H$



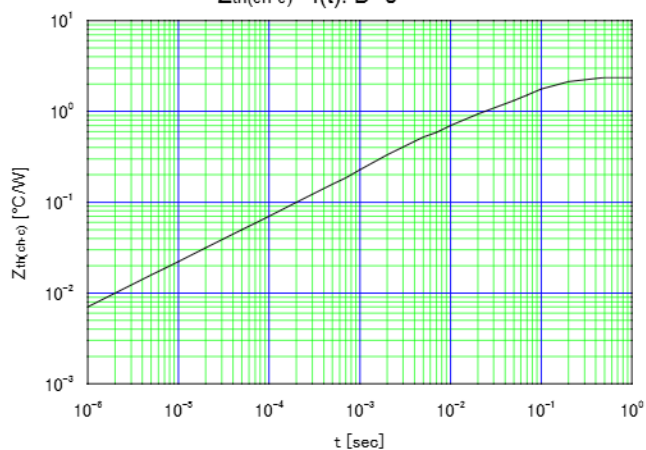
Typical Gate Charge Characteristics
 $V_{GS} = f(Q_g)$: $I_D=20A$, $V_{dd}=480V$, $T_{ch}=25^\circ C$



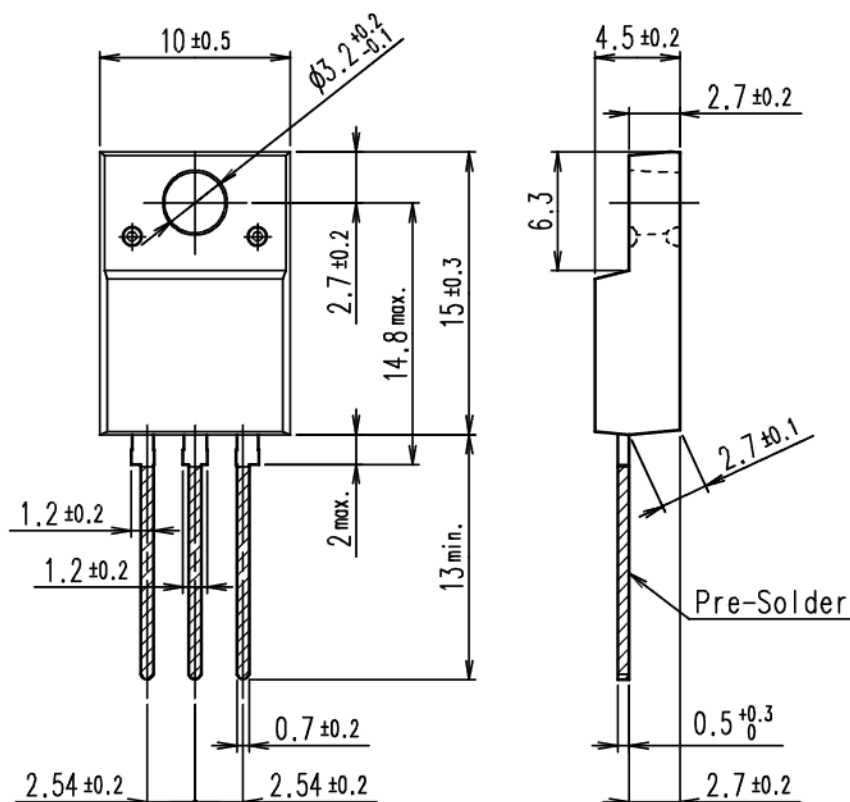
Maximum Avalanche Energy vs. starting T_{ch}
 $E_{AV} = f(\text{starting } T_{ch})$: $V_{CC}=60V$, $I_{(AV)} \leq 6.6A$



Transient Thermal Impedance
 $Z_{th(ch-c)} = f(t)$: $D=0$



■ Outview: TO-220F(SLS) Package

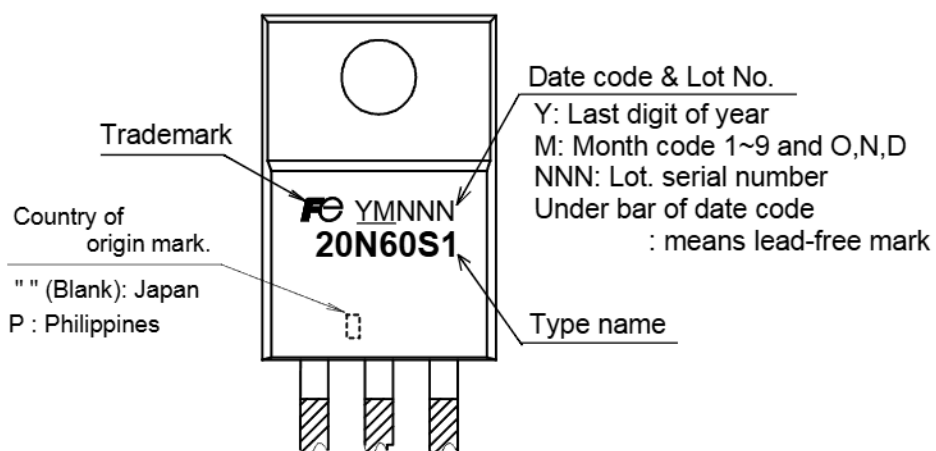


Connection

- ① Gate
- ② Drain
- ③ Source

DIMENSIONS ARE IN MILLIMETERS.

■ Marking



* The font (font type,size) and the trademark-size might be actually different.

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