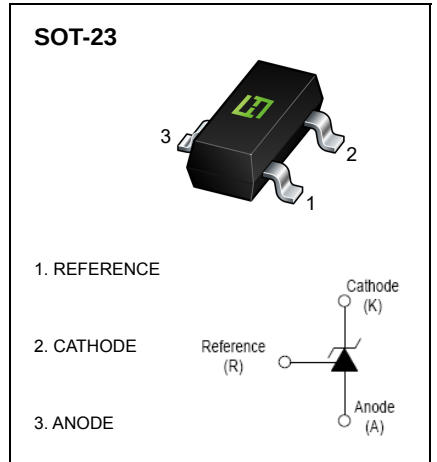


DEVICE DESCRIPTION

The TL431 is a three-terminal adjustable shunt regulator offering excellent temperature stability. This device has a typical dynamic output impedance of 0.2Ω. The device can be used as a replacement for zener diodes in many applications.

FEATURES

- The output voltage can be adjusted to 36V
- Low dynamic output impedance, its typical value is 0.2Ω
- Trapping current capability is 1 to 100mA
- Low output noise voltage
- Fast on-state response
- The effective temperature compensation in the working range of full temperature
- The typical value of the equivalent temperature factor in the whole temperature scope is 50 ppm/°C



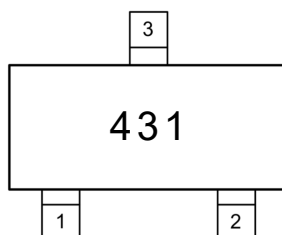
APPLICATION

- Shunt Regulator
- High-Current Shunt Regulator
- Precision Current Limiter

ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

Parameter	Symbol	Value	Unit
Cathode Voltage	V_{KA}	36	V
Cathode Current Range (Continuous)	I_{KA}	-100~+150	mA
Reference Input Current Range	I_{ref}	0.05~+10	mA
Power Dissipation	P_D	300	mW
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	417	°C/W
Operating Temperature	T_{opr}	0~+70	°C
Junction Temperature	T_J	-55~+150	°C
Storage Temperature Range	T_{STG}	-55~+150	°C

MARKING



ELECTRICAL CHARACTERISTICS (T_a=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Reference input voltage (Fig.1)	V _{ref}	V _{KA} =V _{REF} , I _{KA} =10mA	2.450	2.5	2.550	V
Deviation of reference input voltage over temperature (note) (Fig.1)	ΔV _{ref} /ΔT	V _{KA} =V _{REF} , I _{KA} =10mA T _{min} ≤T _a ≤T _{max}		4.5	17	mV
Ratio of change in reference input voltage to the change in cathode voltage (Fig.2)	ΔV _{ref} / ΔV _{KA}	I _{KA} =10mA	ΔV _{KA} =10V~V _{REF}	-1.0	-2.7	mV/V
			ΔV _{KA} =36V~ 10V	-0.5	-2.0	mV/V
Reference input current (Fig.2)	I _{ref}	I _{KA} = 10mA,R ₁ =10kΩ R ₂ =∞		1.5	4	μA
Deviation Of reference input current over full temperature range (Fig.2)	ΔI _{ref} /ΔT	I _{KA} =10mA, R ₁ =10kΩ R ₂ =∞ T _a =full Temperature		0.4	1.2	μA
Minimum cathode current for regulation (Fig.1)	I _{KA(min)}	V _{KA} =V _{REF}		0.45	1.0	mA
Off-state cathode Current (Fig.3)	I _{KA(OFF)}	V _{KA} =36V,V _{REF} =0		0.05	1.0	μA
Dynamic impedance	Z _{KA}	V _{KA} =V _{REF} , I _{KA} =1 to 100mA f≤1.0kHz		0.15	0.5	Ω

note: T_{MIN}=0°C, T_{MAX}=+70°C

CLASSIFICATION of V_{ref}

Rank	0.5%	1%
Range	2.487-2.513	2.475-2.525

Figure 1. Test Circuit for V_{KA} = V_{ref}

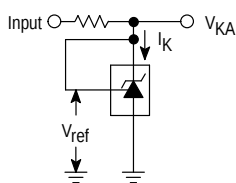


Figure 2. Test Circuit for V_{KA} > V_{ref}

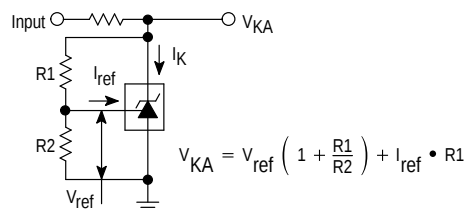
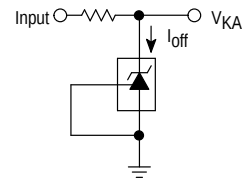
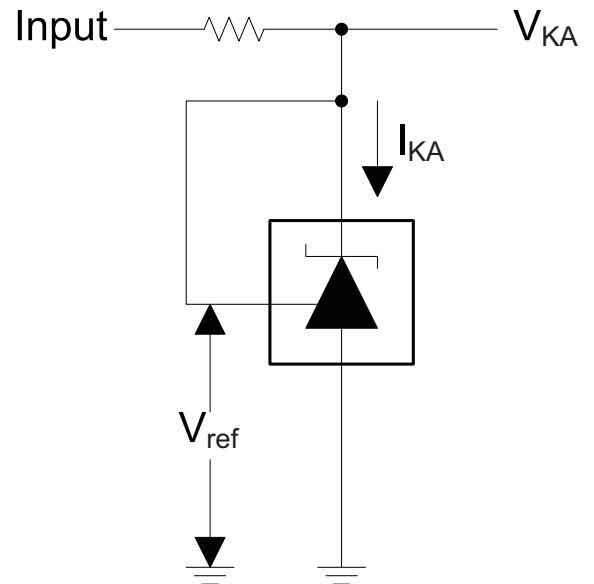
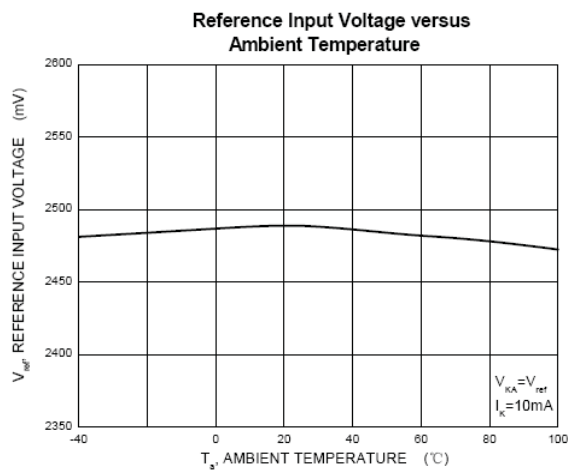
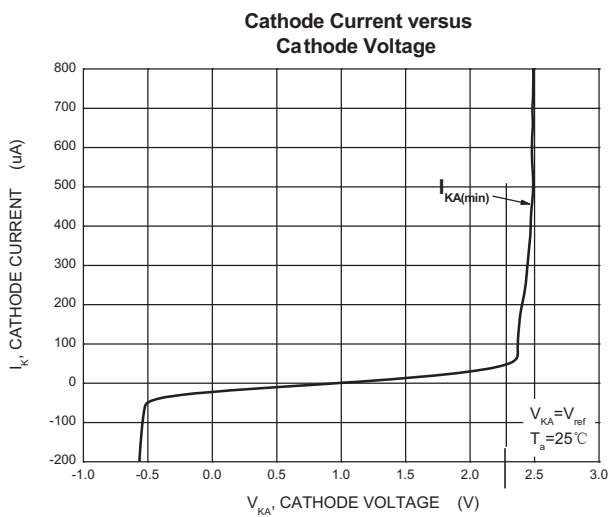
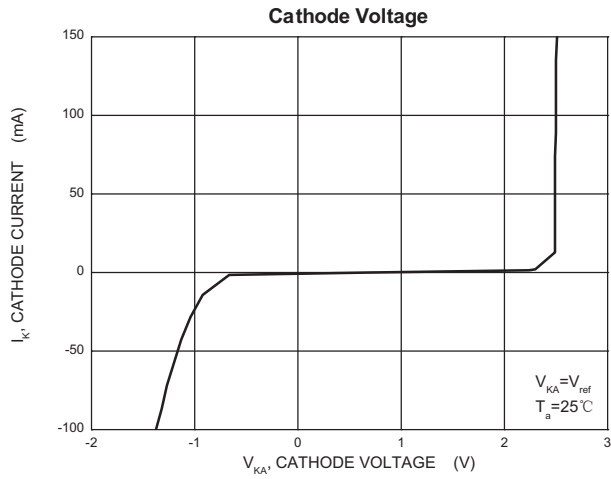


Figure 3. Test Circuit for I_{off}

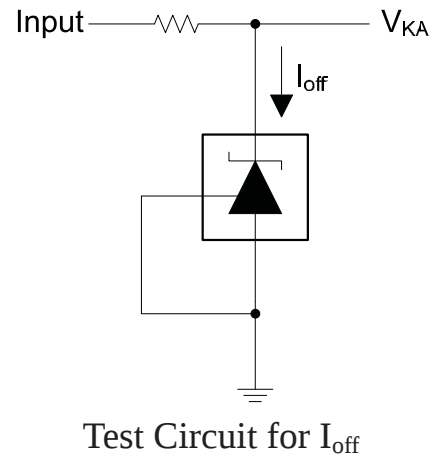
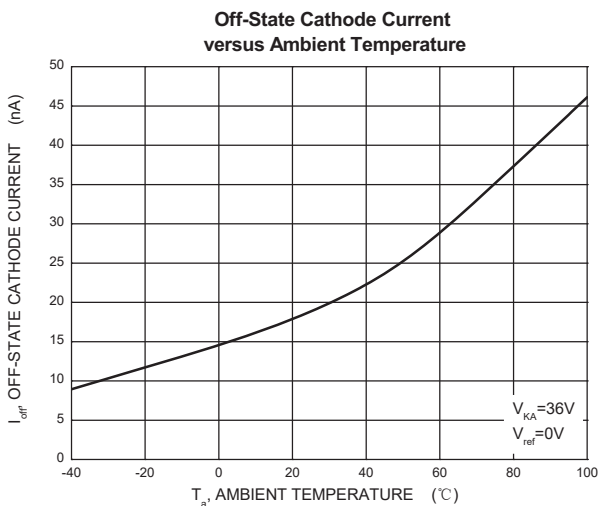
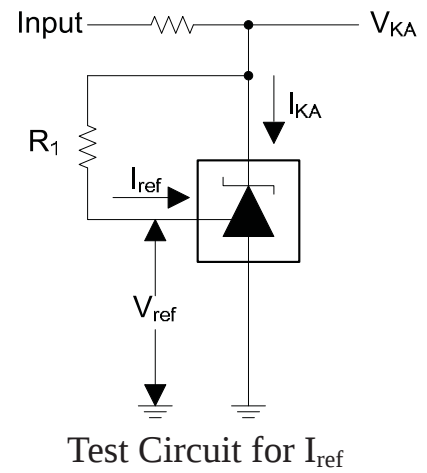
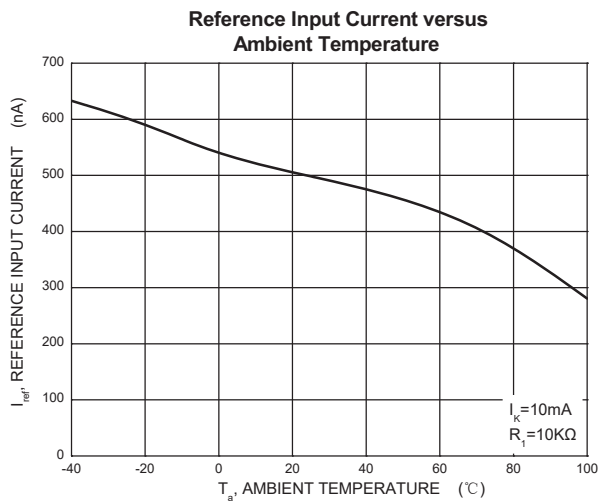
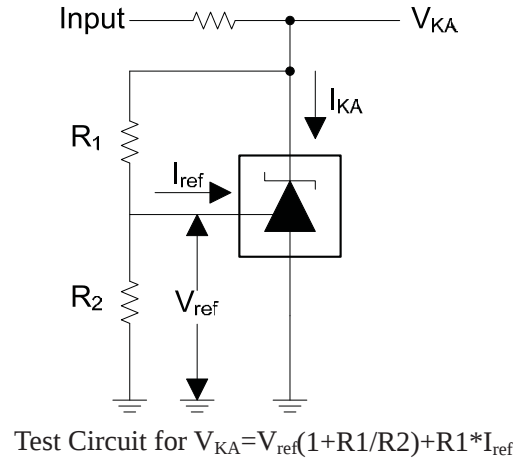
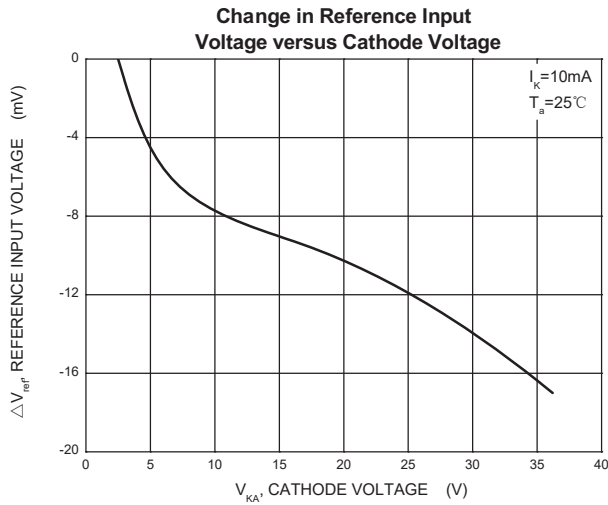


Typical Characteristics

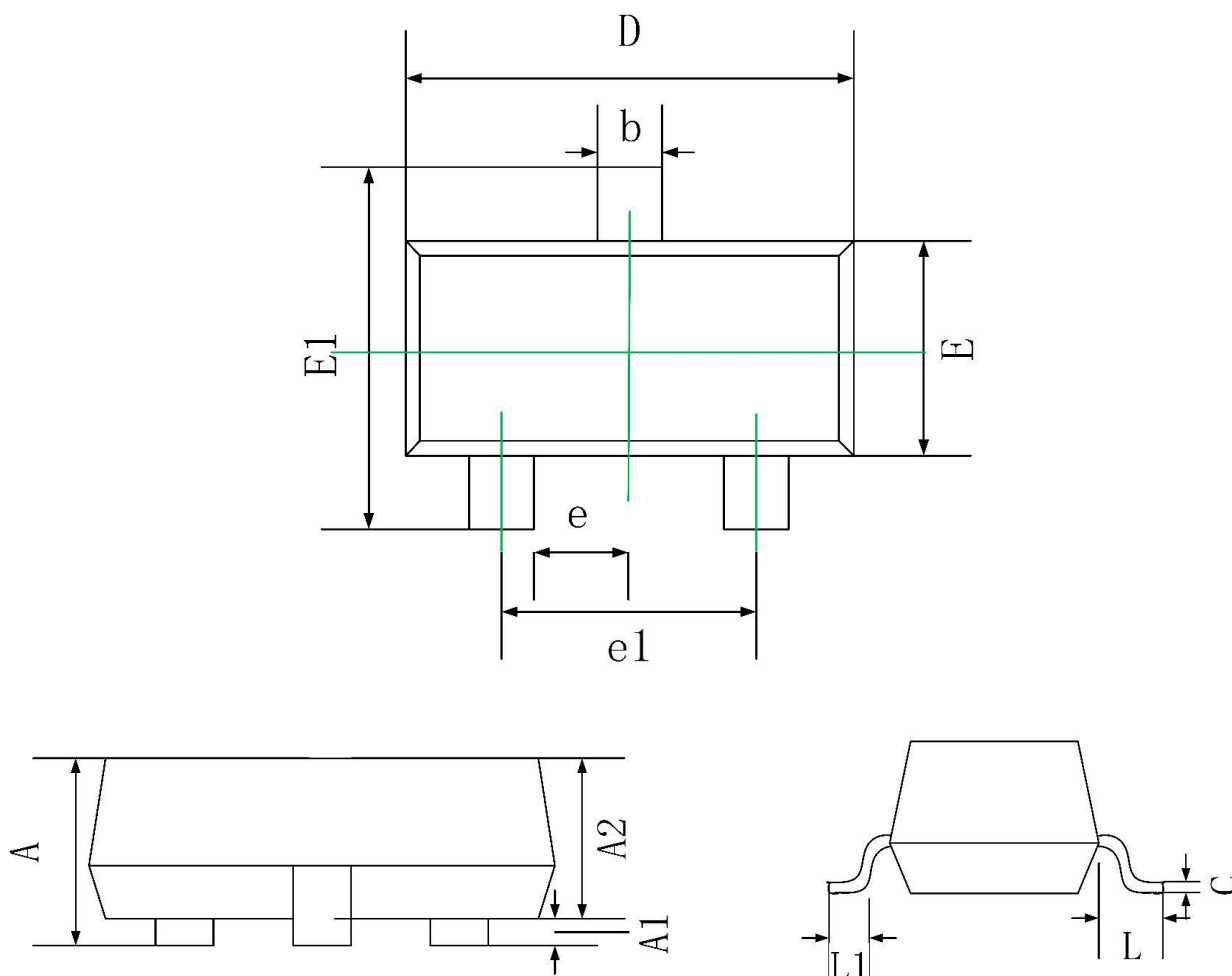


Test Circuit for $V_{KA} = V_{ref}$

Typical Characteristics



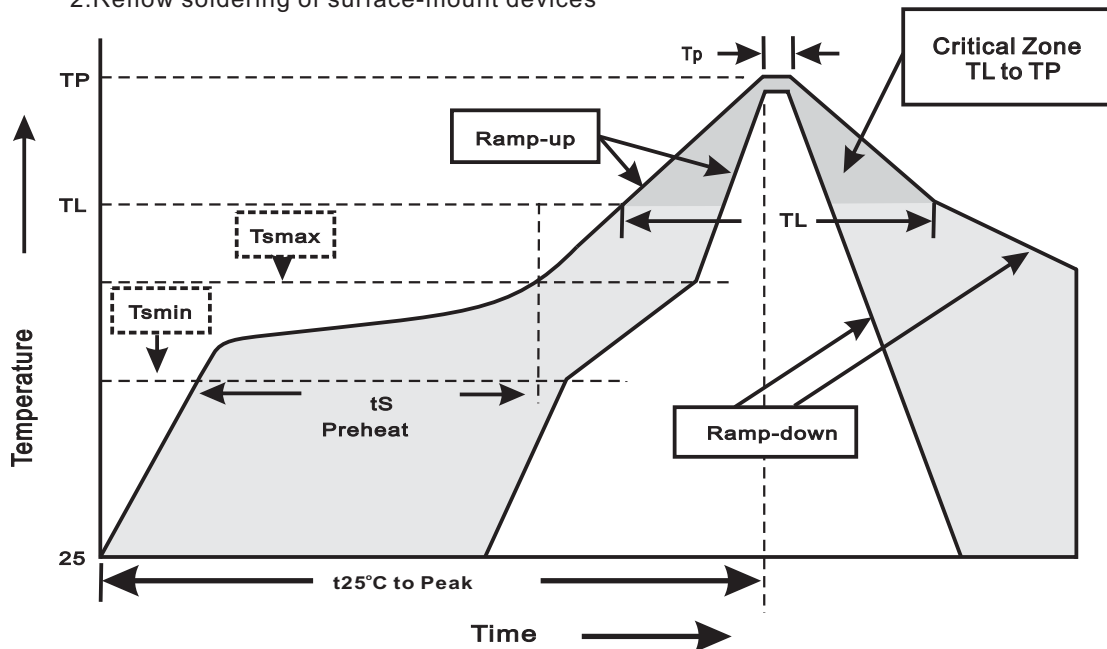
SOT-23 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020

Suggested thermal profiles for soldering processes

- 1.Storage environment: Temperature=5°C~40°C Humidity=55%±25%
2.Reflow soldering of surface-mount devices



3.Reflow soldering

Profile Feature	Soldering Condition
Average ramp-up rate(T _L to T _P)	<3°C/sec
Preheat -Temperature Min(T _{smin}) -Temperature Max(T _{smax}) -Time(min to max)(t _s)	150°C 200°C 60~120sec
T _{smax} to T _L -Ramp-upRate	<3°C/sec
Time maintained above: -Temperature(T _L) -Time(t _L)	217°C 60~260sec
Peak Temperature(T _P)	255°C-0/+5°C
Time within 5°C of actual Peak Temperature(t _P)	10~30sec
Ramp-down Rate	<6°C/sec
Time 25°C to Peak Temperature	<6minutes