# MSKSEMI 美森科













**ESD** 

TVS

TSS

MOV

GDT

PLED

WNM2021-3/MS

Product specification





#### **FEATURES**

- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- ESD Protected:1000V

## MAXIMUM RATINGS(Ta = 25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	VDSS	20	V
Drain–Gate Voltage (RGS = 1.0 M $\Omega$ )	VDGR	6	V
Drain Current	ID		mA
- Continuous TC = 25 °C		±615	
TC = 100°C		± 575	
- Pulsed (Note 1)	IDM	±800	
Gate-Source Voltage			
- Continuous	VGS	±20	V
<ul><li>Non–repetitive (tp≤50 µs)</li></ul>	VGSM	±40	V

#### **Reference News**

PACKAGE OUTLINE	PIN Configuration	Marking
D	D 3 1 1 G S	6C*
SOT-323		

## THERMAL CHARACTERISTICS

Parameter	Symbol	Limits	Unit
Total Device Dissipation, FR-5 Board (Note 2) @ TA = 25°C	PD	225	mW
Derate above 25° C		1.8	mW/°C
Thermal Resistance, Junction– to– Ambient( Note 2 )	RΘJA	556	°C/W
Junction and Storage temperature	TJ,Tstg	-55~ +15 0	°C

- 1. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.
- 2.  $FR-5 = 1.0 \times 0.75 \times 0.062$  in.



## **ELECTRICAL CHARACTERISTICS (Ta= 25°C)**

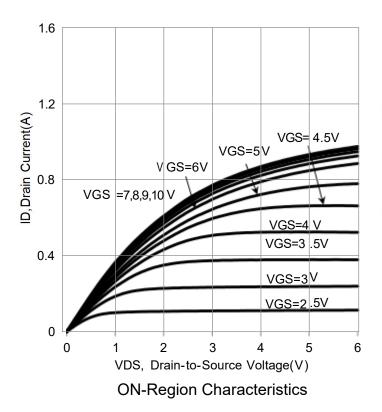
#### OFF CHARACTERISTICS

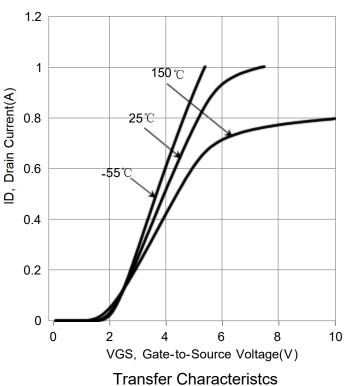
Chara	acteristic	Symbol	Min.	Тур.	Max.	Unit
Drain-Source Breakdo (VGS = 0, ID = 10μA)	own Voltage	VBRDSS	20	-	-	V
Zero Gate Voltage Drain Current TJ = 25 °C (VGS = 0, VDS = 2 0 V) TJ = 125 °C		IDSS	-	-	1.0	
		וטסס	-	-	500	μA
Gate–Body Leakage Cu (VGS = 20 V)	urrent, Forward	IGSSF	-	-	1.0	μА
Gate–Body Leakage Co (VGS = - 20 V)	urrent, Reverse	IGSSR	-	-	-1.0	μА
ON CHARACTERI	STICS (Note 3)			1		I
Gate Threshold Voltage (VDS = VGS, ID = 250		VGS(th)	1.0	1.6	2.5	V
On–State Drain Curren (VDS ≥2.0 VDS(on), V		ID(on)	500	-	-	mA
Static Drain-Source (VGS = 10 V, ID = 500 (VGS = 5.0 V, ID = 50	mA)	VDS( on)	-	- -	3.75 0.375	V
Static Drain-Source Or	n–State Resistance		<del>-</del>	_		Ohms
(VGS = 10 V, ID = 500	) mA)	RDS(on)	-	-	7.5 13.5	
(VGS = 5.0 V, ID = 50 mA)			- - -	- - -	7.5 13.5	
Forward Transconducta (VDS≥ 2.0 VDS(on), ID		gfs	80	-	-	mmhos
DYNAMIC CHARA	ACTERISTICS					
Input Capacitance (VDS = 25 V, VGS = 0	, f = 1.0 MHz)	Ciss	-	17	50	pF
Output Capacitance (VDS = 25 V, VGS = 0	, f = 1.0 MHz)	Coss	-	10	25	pF
Reverse Transfer Capa (VDS = 25 V, VGS = 0		Crss	-	2.5	5.0	pF
WITCHING CHAR	RACTERISTICS	_				
Turn-On Delay Time	(VDD = 25 V , ID =500	td(on)	-	7	20	ns
Turn-Off Delay Time	mA, RG = $25\Omega$ ,RL = $50$ $\Omega$ ,Vgen = $10$ V)	td(off)	-	11	40	
BODY-DRAIN D	IODE RATINGS	1		-	1	I
Diode Forward On–Vol (IS = 115 mA, VGS =	•	VSD	-	-	1.5	V
Source Current Continuous (Body Diode)		IS	-	-	115	mA
Source Current Pulsed		ISM	-	-	800	mA
Dulca Tact: Dulca Wid	th ≤300 µs, Duty Cycle ≤2	00/		I		

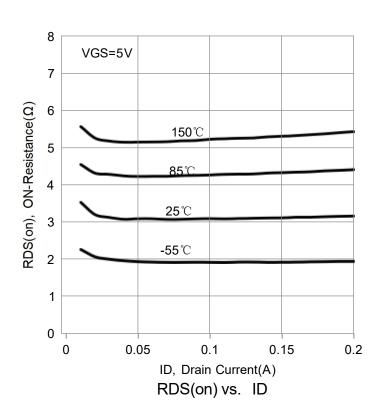
3. Pulse Test: Pulse Width ≤300 µs, Duty Cycle ≤2.0%.

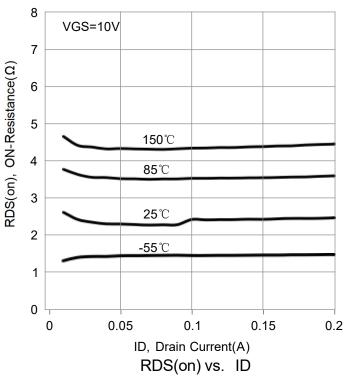


#### **ELECTRICALCHARACTERISTICSCURVES**



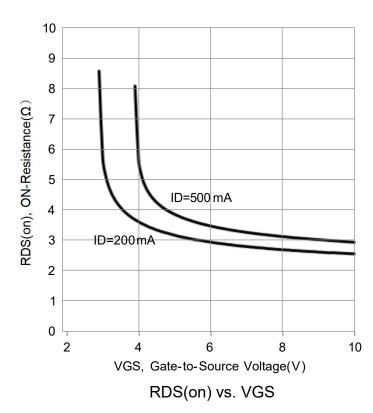


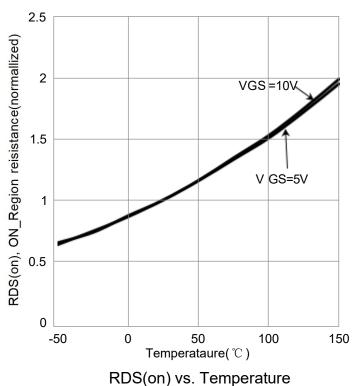


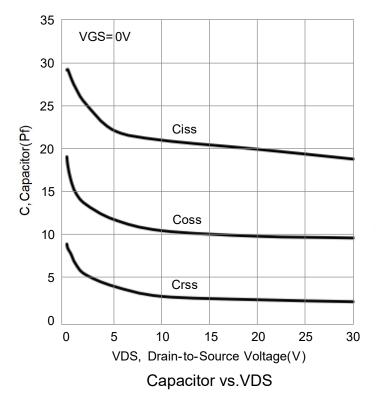


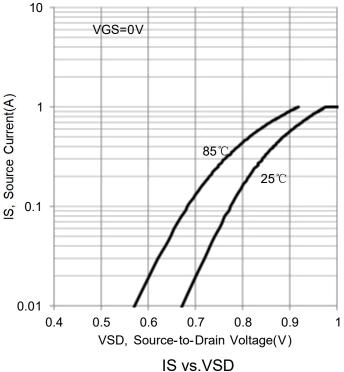


## ELECTRICALCHARACTERISTICSCURVES (Con.)



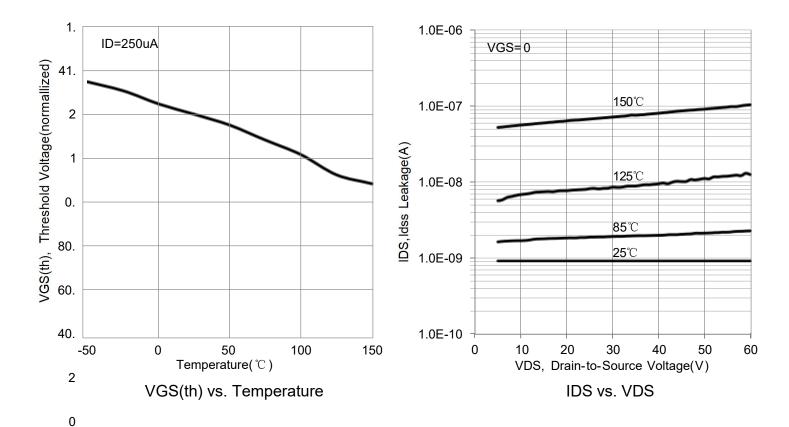






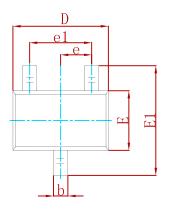


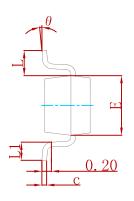
## ELECTRICALCHARACTERISTICSCURVES (Con.)

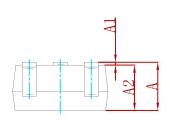




## **PACKAGEMECHANICALDATA**

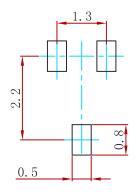






Symbol	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min	Max	Min	Max
Α	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.200	0.400	0.008	0.016
С	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
Е	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
е	0.650	) TYP	0.026	TYP
e1	1.200	1.400	0.047	0.055
L	0.525	REF	0.021	I REF
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

## **Suugested Pad Layout**



#### Note:

- 1. Controlling dimension:in millimeters.
- 2.General tolerance:±0.05mm.
- 3. The pad layout is for reference purposes only.

## **REELSPECIFICATION**

P/N	PKG	QTY
WNM2021-3/MS	SOT-323	3000



#### **Attention**

- Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.
- MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all MSKSEMI Semiconductor products described or contained herein.
- Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer'sproducts or equipment.
- MSKSEMI Semiconductor. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with someprobability. It is possiblethat these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits anderror prevention circuitsfor safedesign, redundant design, and structural design.
- In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from theauthorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. Whendesigning equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor productthat you intend to use.